

Model Name: GA-B250M-EVO

rev 1.0

SHEET

TITLE

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02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1151-A
05	CPU_LGA1151-B-DDR4
06	CPU_LGA1151-C
07	CPU_LGA1150-D
08	DDR4 CHANNEL A
09	DDR4 CHANNEL B
10	PCH_CLK BUFFER
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH SATA,PCIE,SATA_EXPRESS
14	PCH PWR
15	PCH GND
16	ITE 8686 LPC IO
17	HWM
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20	PCI EXPRESSX1 SLOT *2
21	Single BIOS
22	M.2 X4 (Q)
23	M.2X4_S5 SWITCH
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25	PCI SLOT(NA)
26	LED POWER(NA)
27	ISL95858 PWM-IRON
28	ISL95858 VCORE-IRON

SHEET

TITLE

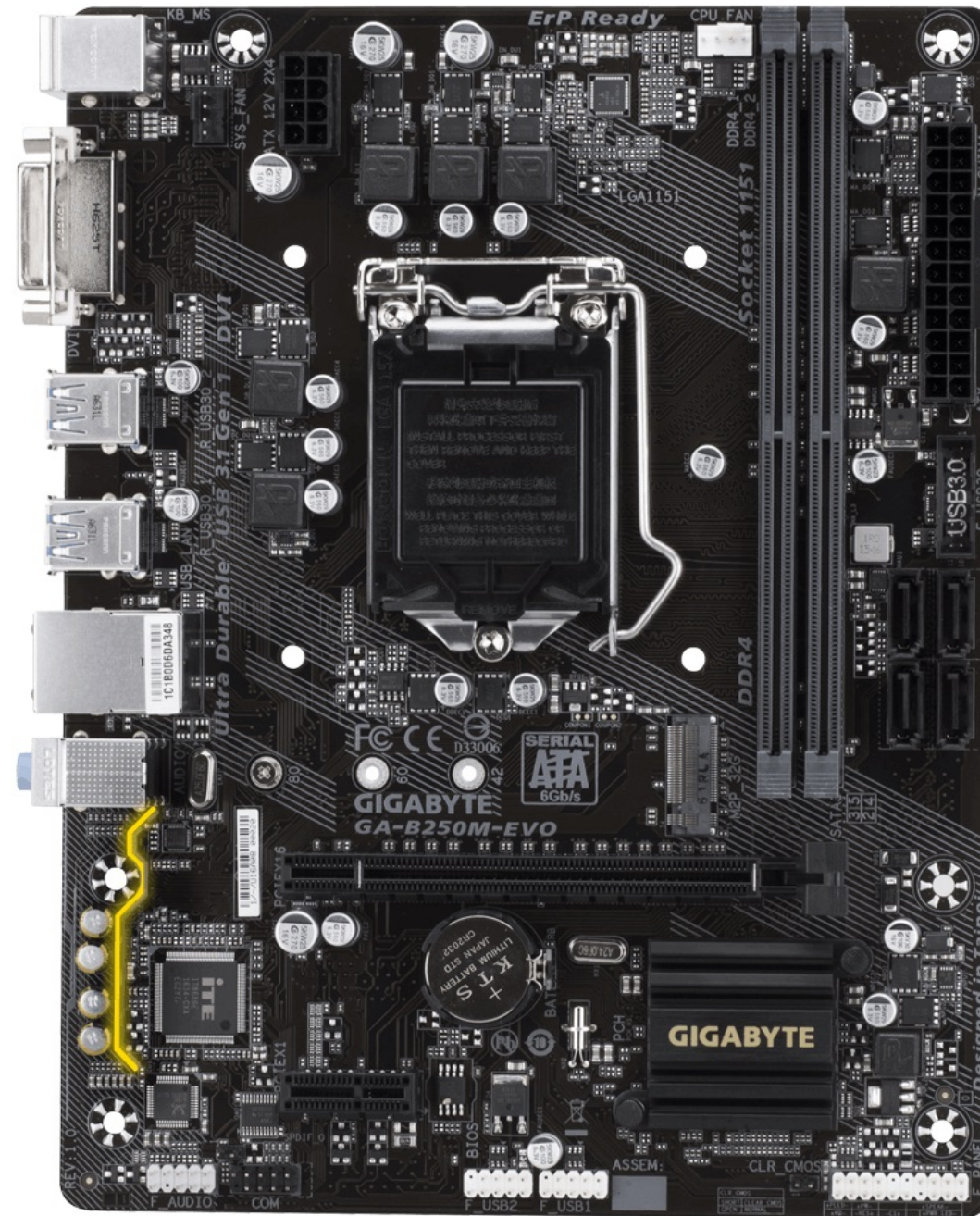
29	ISL95858 VCCGT-IRON
30	VCCSA_VCCIO_VCCPLL
31	RT8237_DDR_BEAD
32	RT8068A_VPP
33	RT8237_PCH-BEAD
34	DISCRETE POWER
35	ATX POWER , A_-PROCHOT
36	KB_MS/PS2
37	DVI CONN
38	RTD2168 - DP to VGA - IC
39	RTD2168 - DP to VGA - Conn
40	REALTEK 8111G
41	USB30_LAN CONNECTOR-8111G
42	Realtek ALC887
43	REAR AUDIO JACK
44	ADUIO LED
45	R_USB30_1
46	R_USB30_2
47	F_USB30
48	F_USB
49	F_PANEL
50	COM, TPM
51	EMI-ESD
52	POWER MAP
53	NTC MAP
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Title			Cover Sheet
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Custom			Rev 1.0
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Model Name: GA-B250M-EVO *rev1.0*

Component value change history

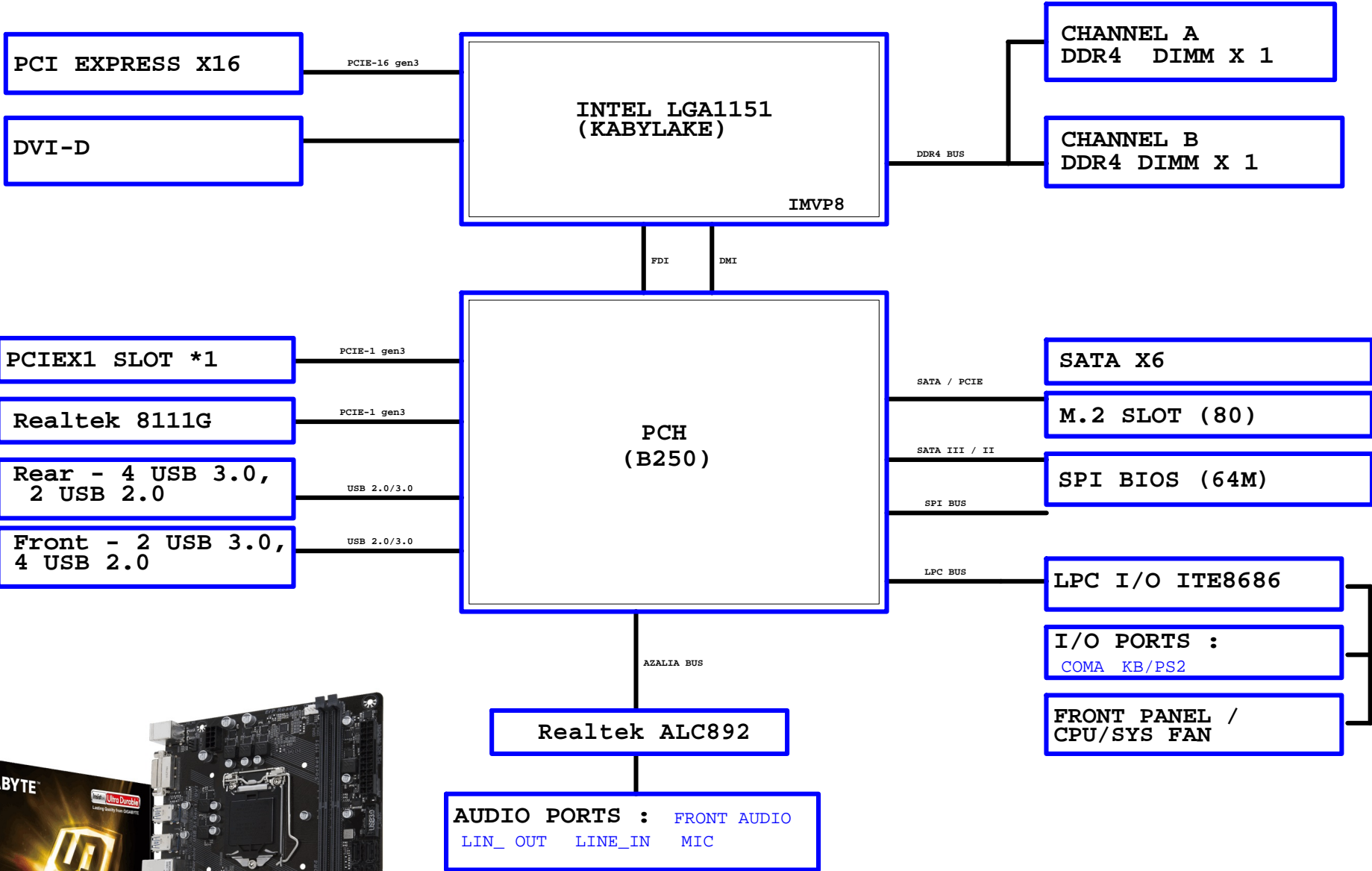
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GIGABYTE™

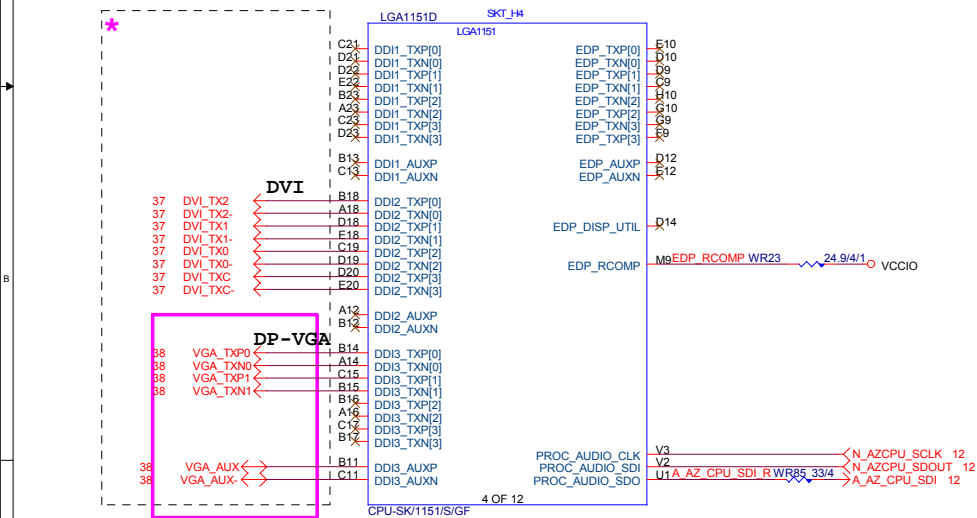
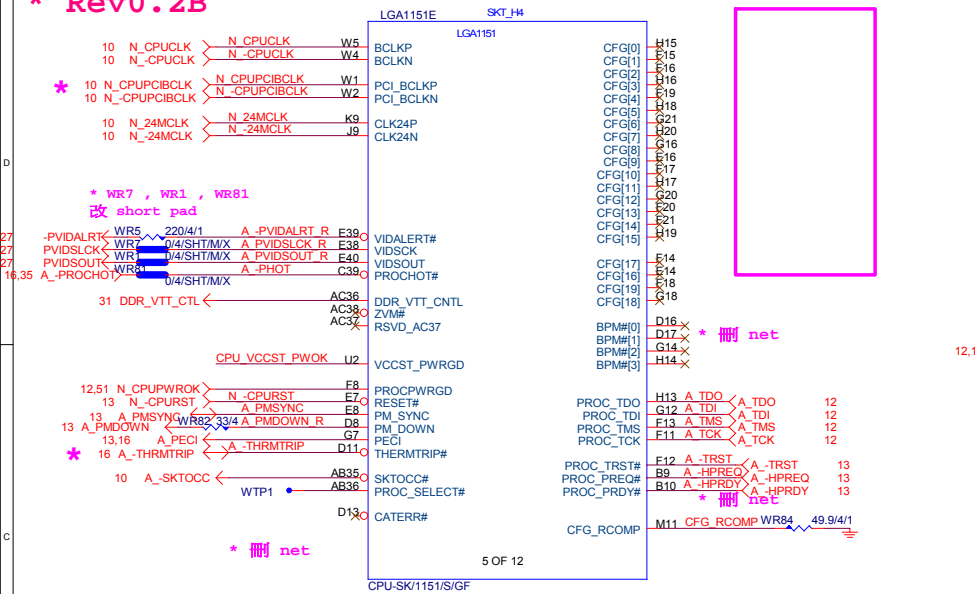
Title	BOM & PCB MODIFY HISTORY
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BLOCK DIAGRAM



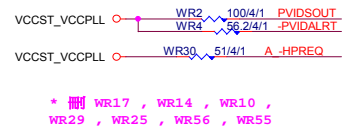
*** Rev0.2B**



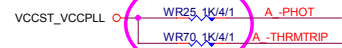
```
G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R
```

```
4 layer HDMI/DP/eDP/=====4/4/4//15
6 layer HDMI/DP/eDP/=====4/5.5/4//15
```

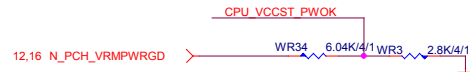
Impedance=85 +- 15%



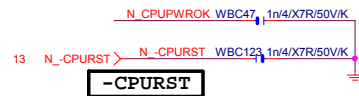
* 刪 WR17 , WR14 , WR10 ,
WR29 , WR25 , WR56 , WR55



* 删除 WR91



```
* 删除 net N_CPU_VCCST_PWOK
```



```

PA_EXP_TXP[0..15]  >> PA_EXP_TXP[0..15]  19
PA_EXP_TXN[0..15]  >> PA_EXP_TXN[0..15]  19
PA_EXP_RXP[0..15]  >> PA_EXP_RXP[0..15]  19
PA_EXP_RXN[0..15]  >> PA_EXP_RXN[0..15]  19

```

```
4 layer PEG/DMI=====4/4/4//15
6 layer PEG/DMI=====4/5.5/4//15
```

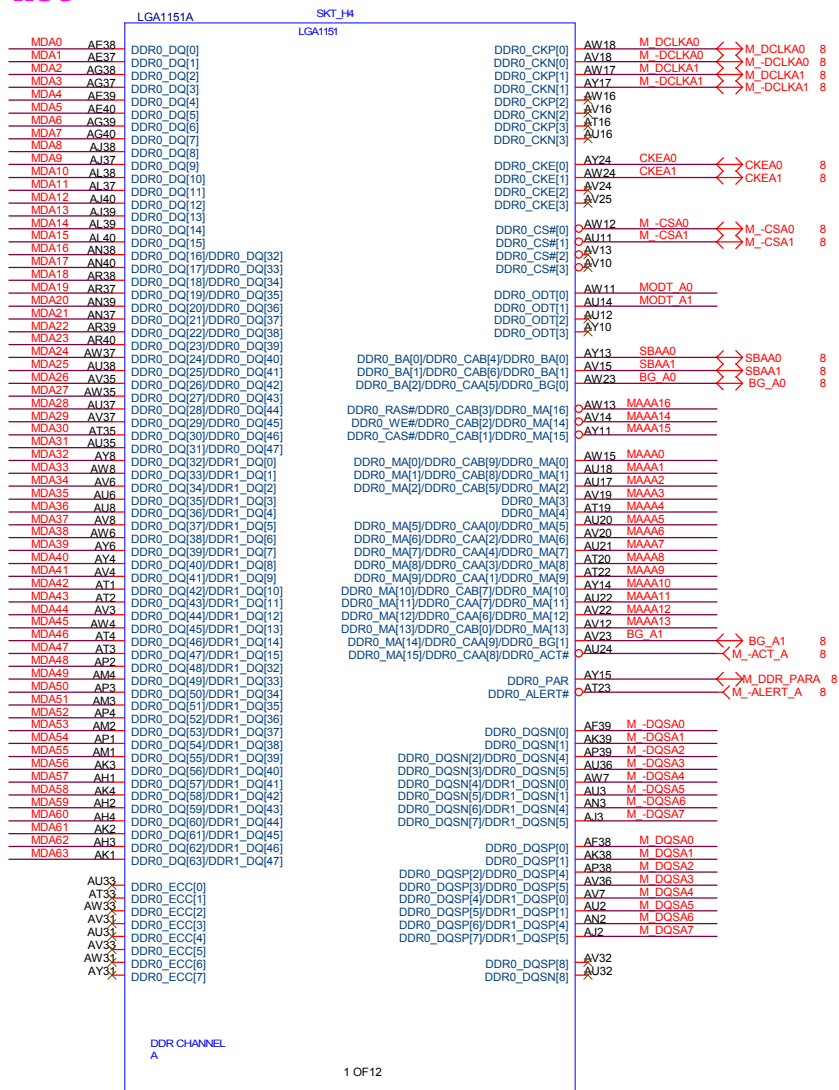
Impedance=85 +- 15%

W=12 mil out of CPU
S=15 mil out of CPU

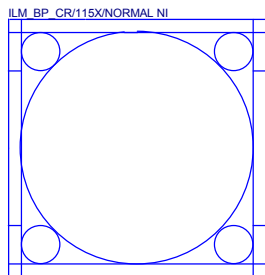
FOR BIOS			
Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0

<p align="center"><i>Gigabyte Technology</i></p>			
<p>Title: CPU LGA1151-A</p>			
Size Custom	Document Number		Rev
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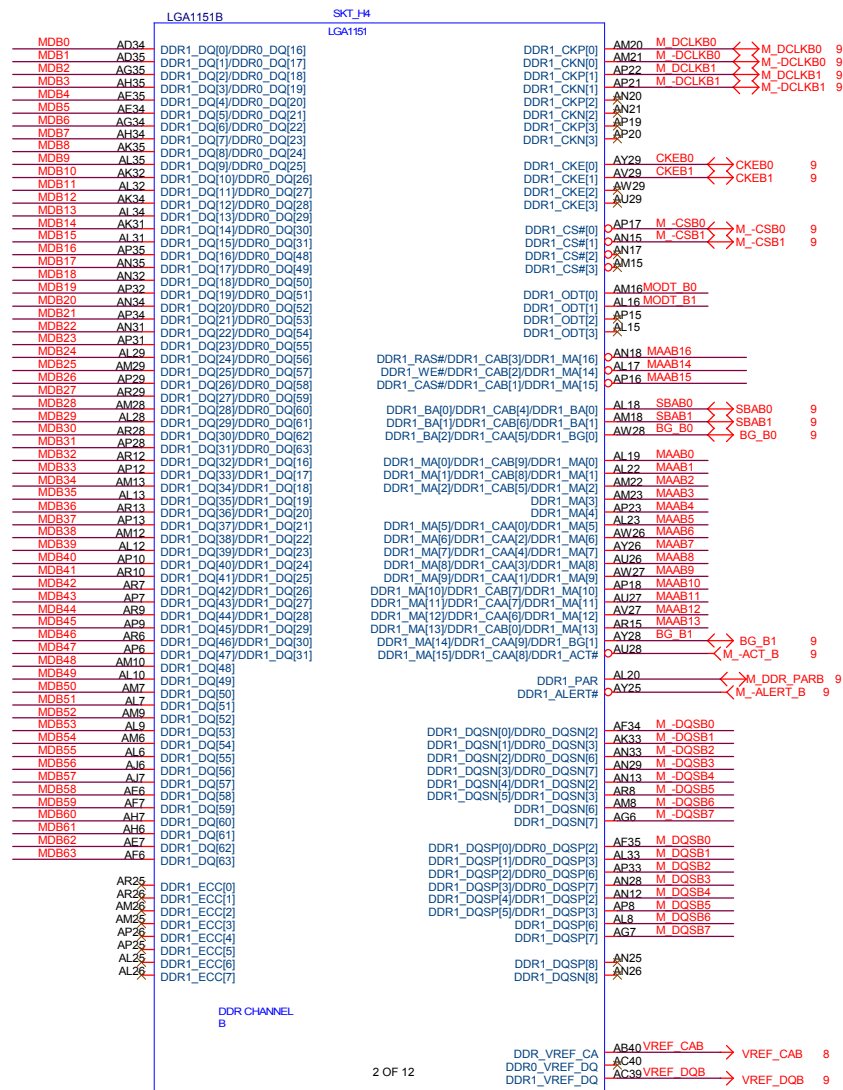
* 改DDR4 net



LGA1151



Need check the new CPU MB

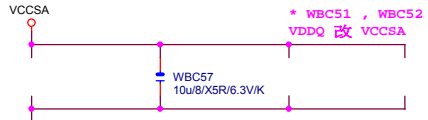


CPU-SK1151/S/GF

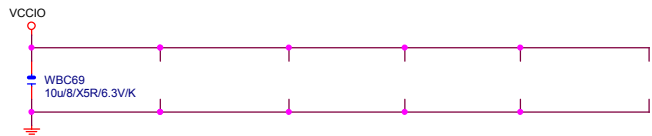
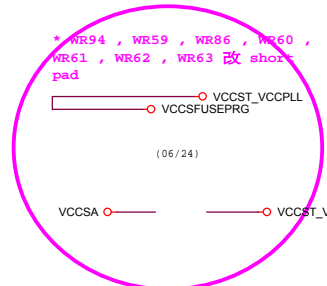
- 8 MODT_A[0..1] <-> MODT_A0..11
- 9 MODT_B[0..1] <-> MODT_B0..11
- 8 MDA[0..63] <-> MDA0..63
- 9 MDB[0..63] <-> MDB0..63
- 8 M_DQSA[0..7] <-> M_DQSA0..71
- 8 M_-DQSA[0..7] <-> M_-DQSA0..71
- 8 MAAA[0..16] <-> MAAA0..161
- 9 MAAB[0..16] <-> MAAB0..161
- 9 M_DQSB[0..7] <-> M_DQSB0..71
- 9 M_-DQSB[0..7] <-> M_-DQSB0..71

Gigabyte Technology		
Title CPU LGA1151-B		
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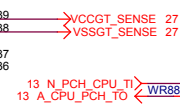
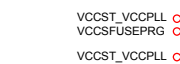
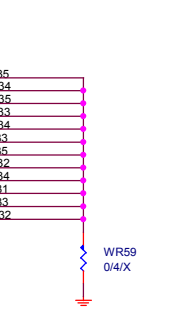
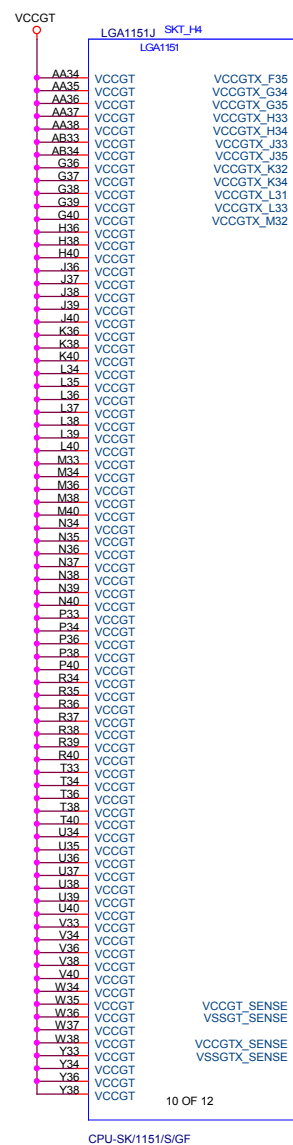
* 刪 WBC50 電容



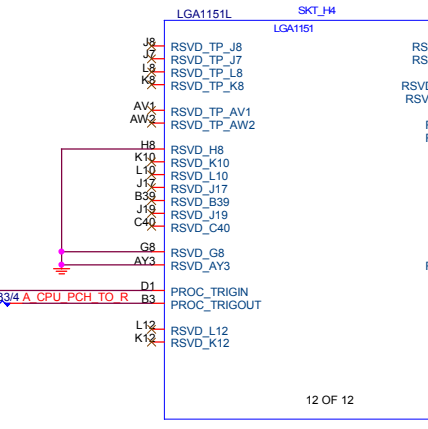
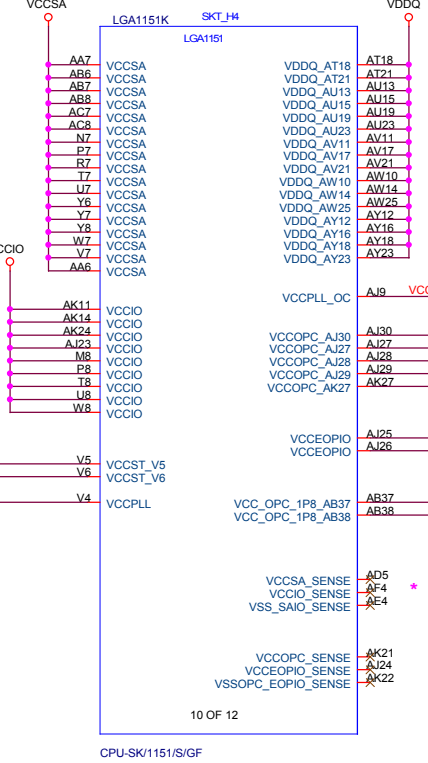
* 刪 WBC124 , WBC125 , WBC126 , WBC127 電容



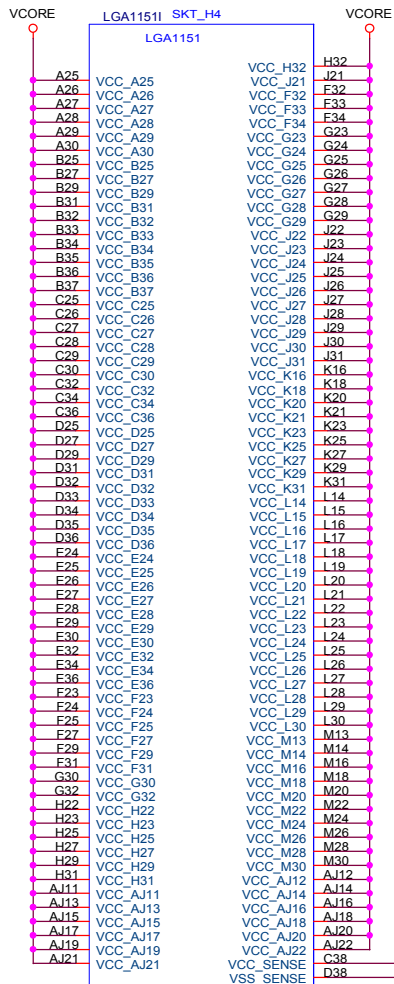
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13 N_PCH_CPU_T1 > WR88 < 33/4 A_CPU_PCH_TO R



CPU-SK/1151/S/GF

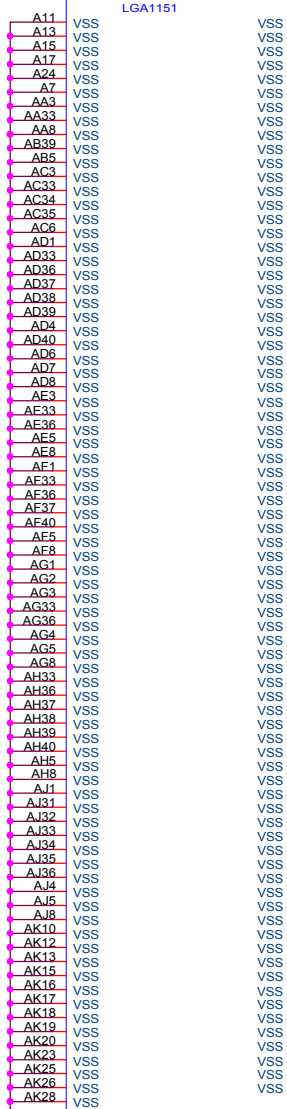


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CPU-SK/1151/S/GF

* 刪 Vcore 電容

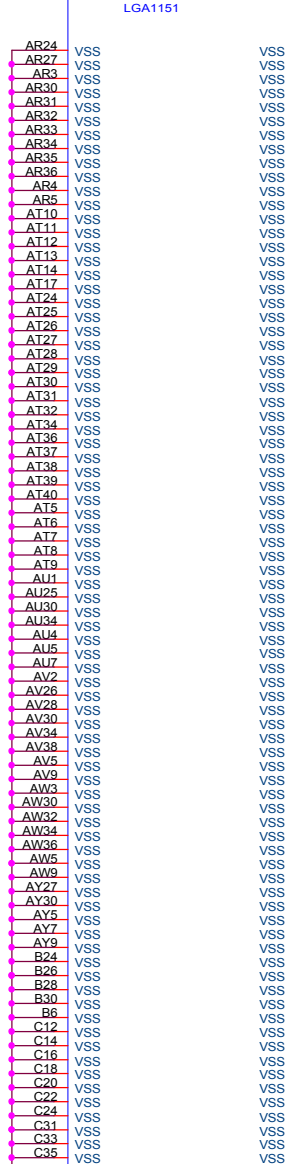
LGA1151F SKT_H4



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CPU-SK/1151/S/GF

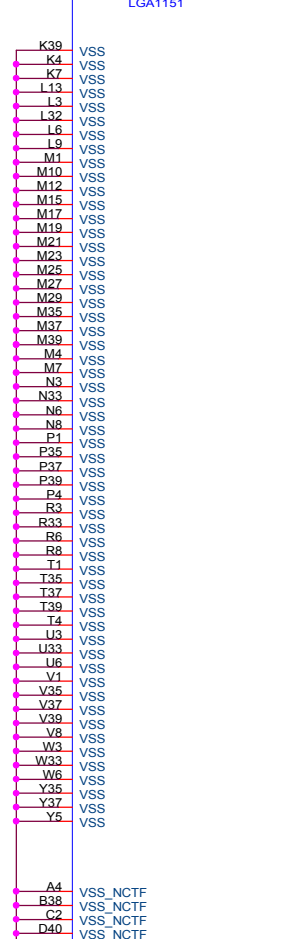
LGA1151G SKT_H4



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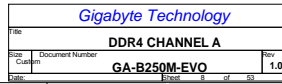
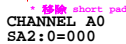
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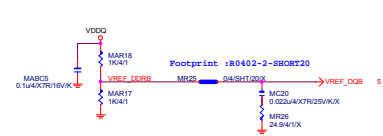
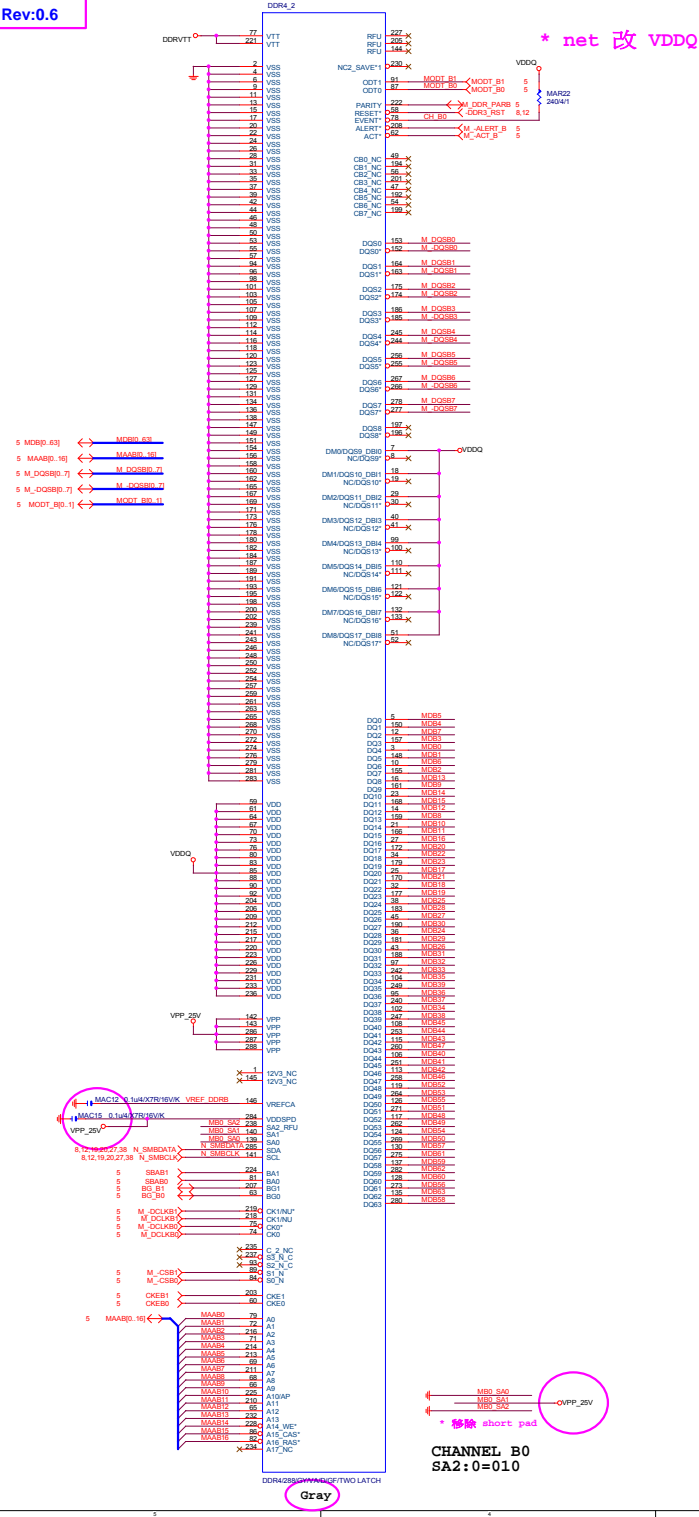
LGA1151H SKT_H4

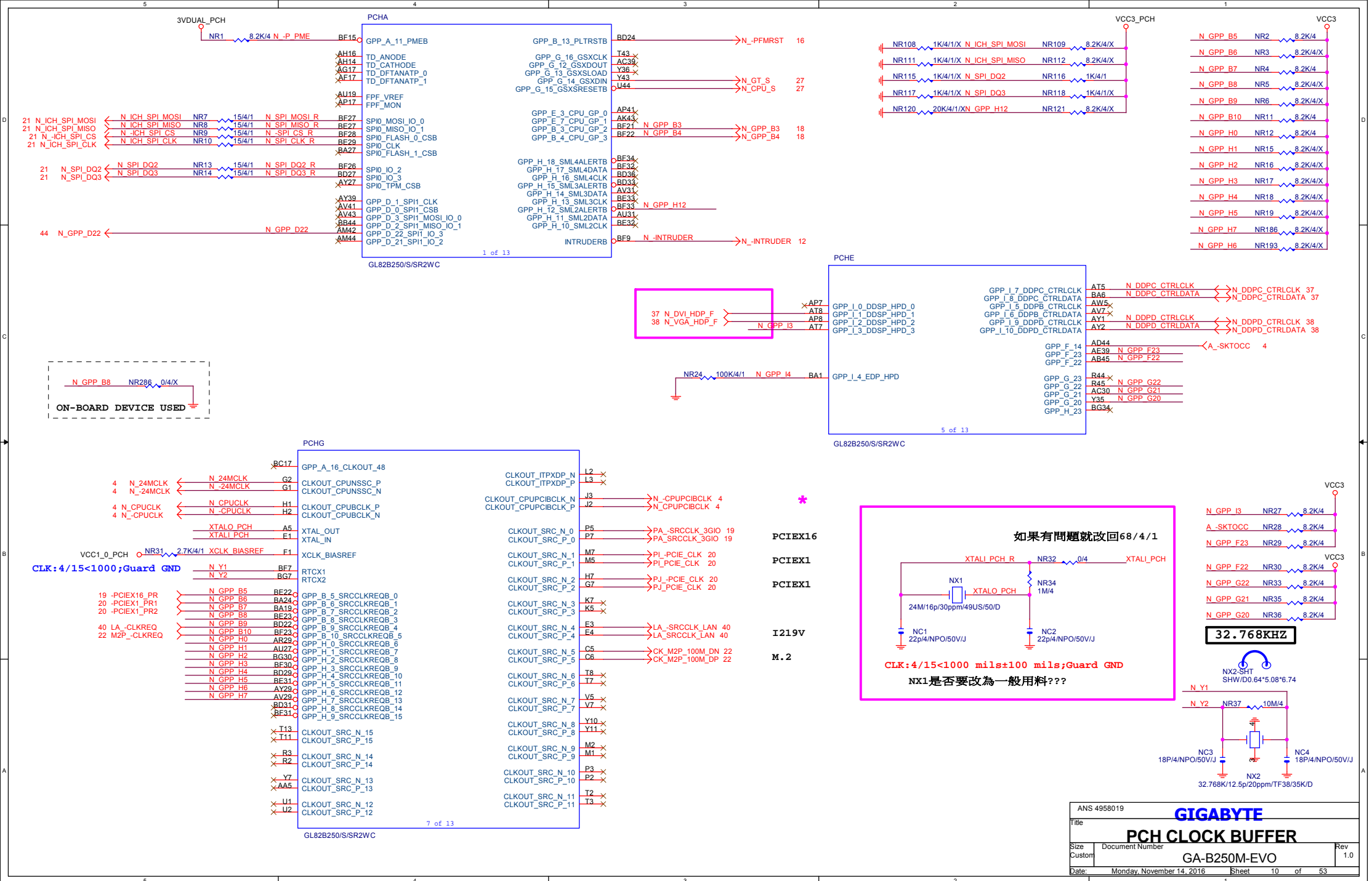


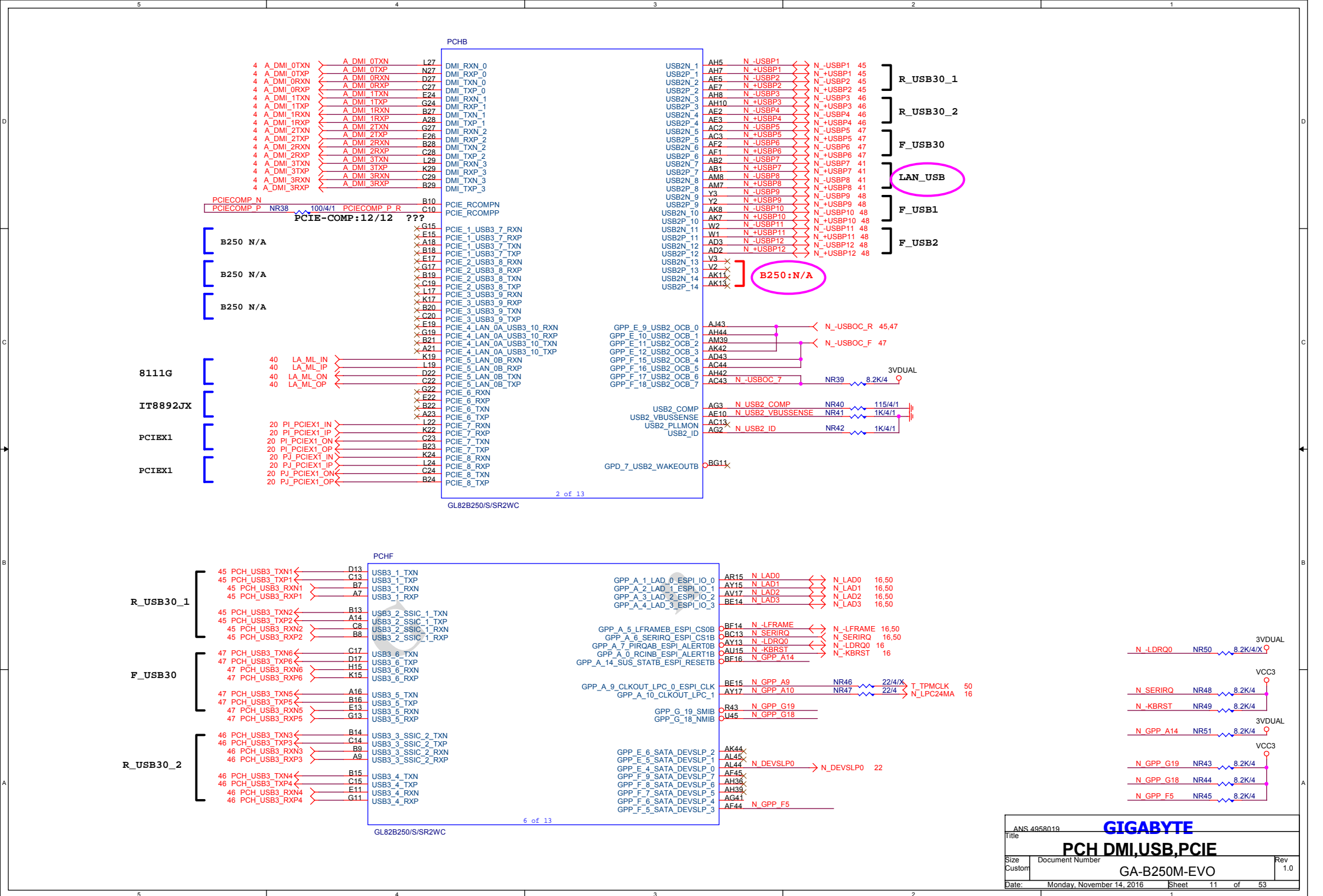
8 OF 12

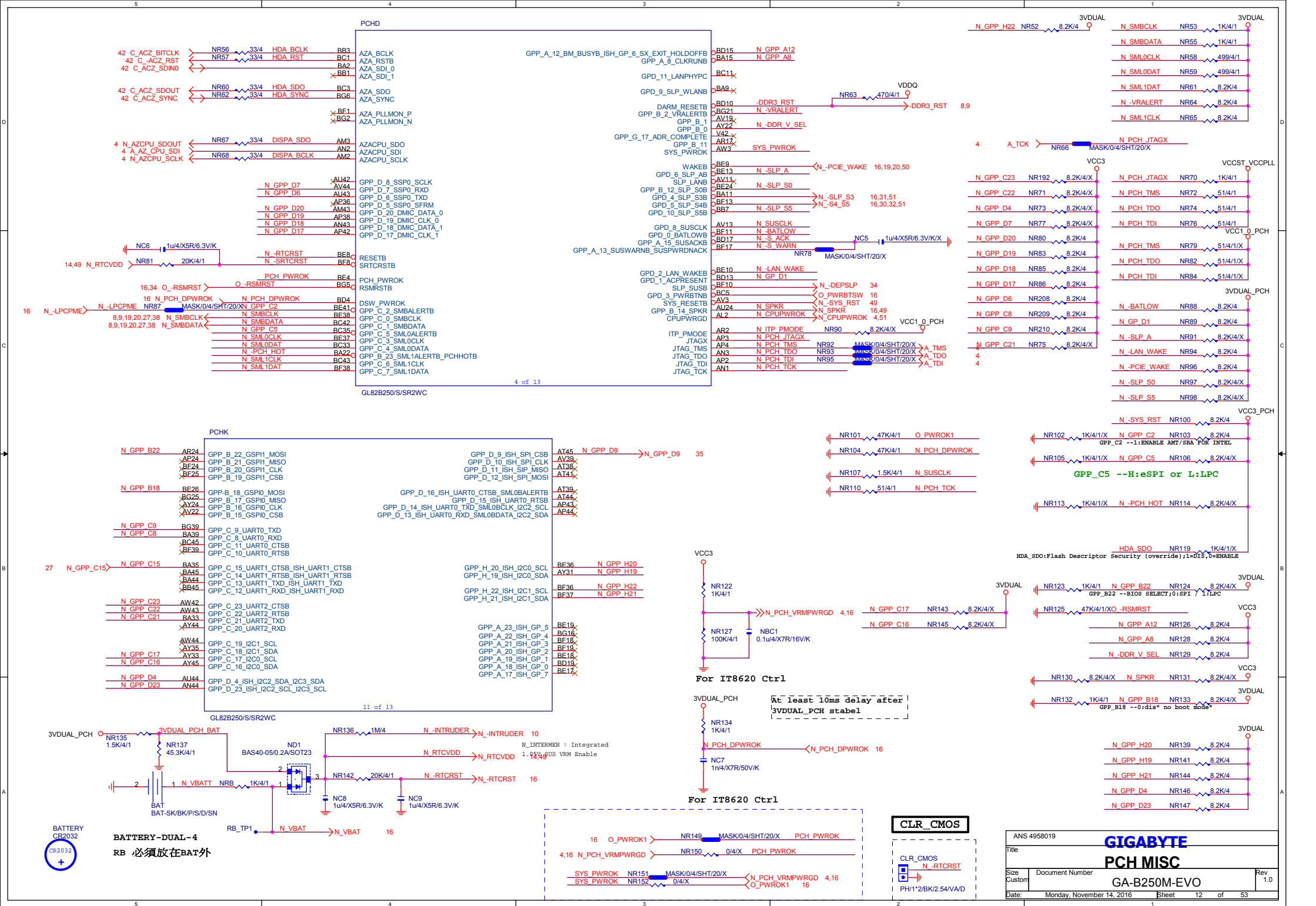
CPU-SK/1151/S/GF

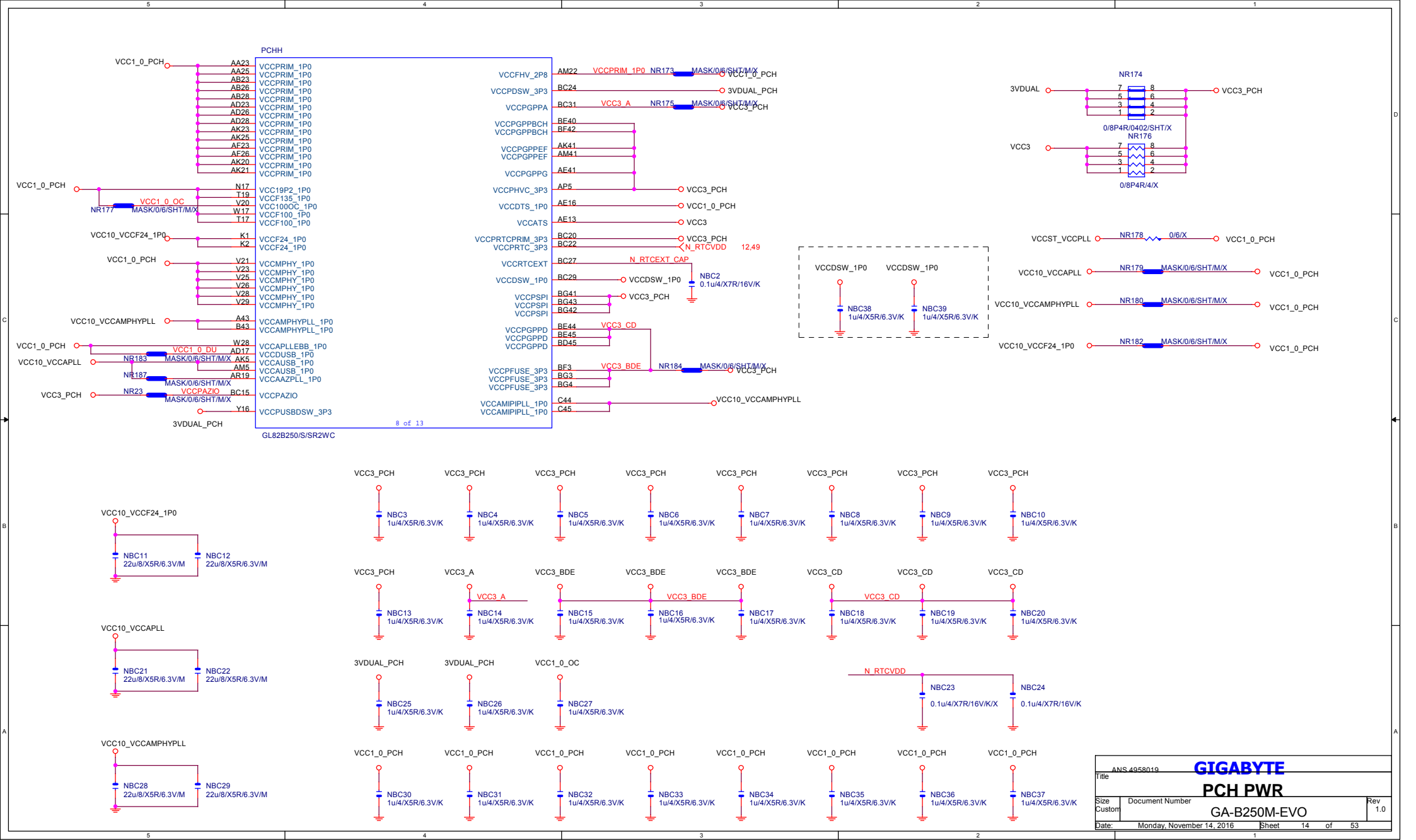


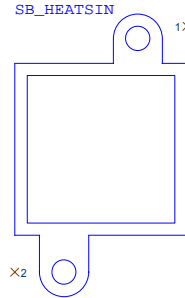
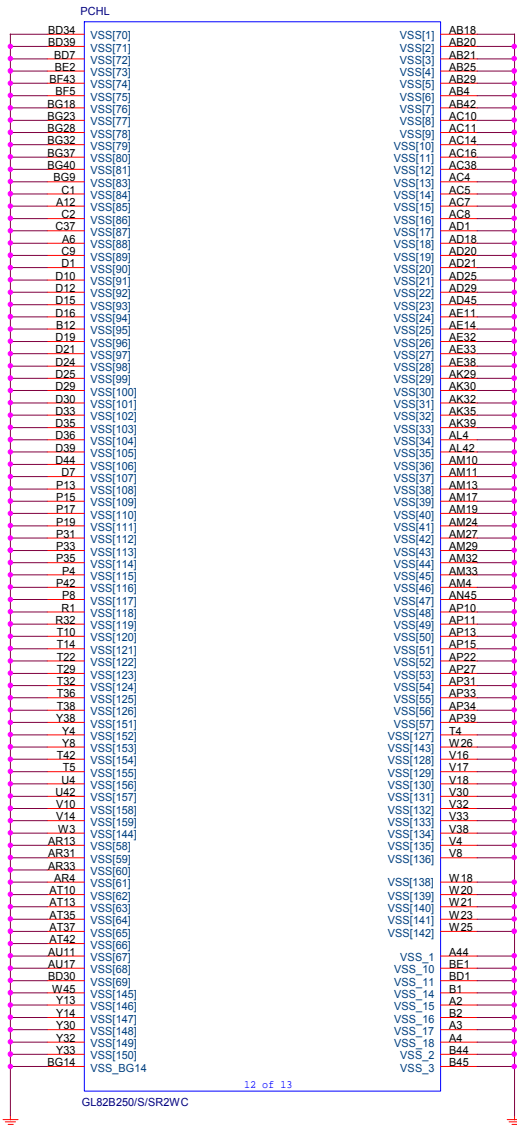
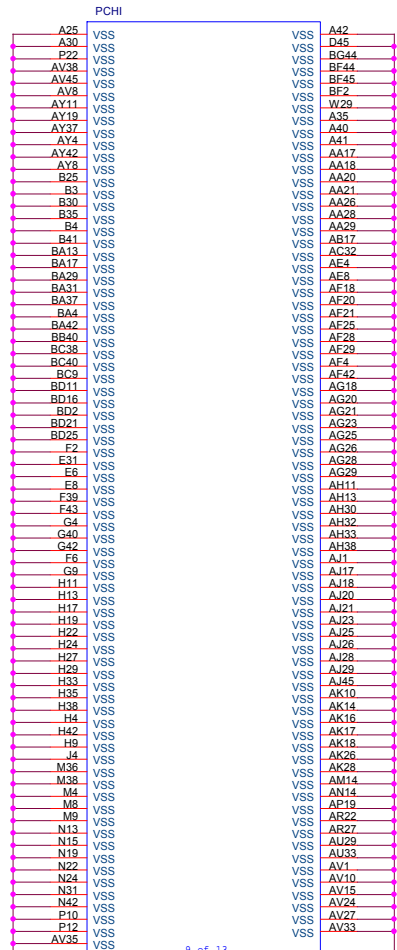












Heatsink

LOW COST ICH7 HEATSINK
BGAHSINK_SB-N

PCH_HS
PCH_HS[12SP2-S03507-61R_12SP2-S03507-62R_12SP2-S03507-63R]

ANS 4958019			
GIGABYTE			
Title			
PCH GND			
Size	Document Number	Rev	
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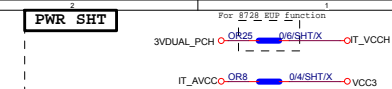


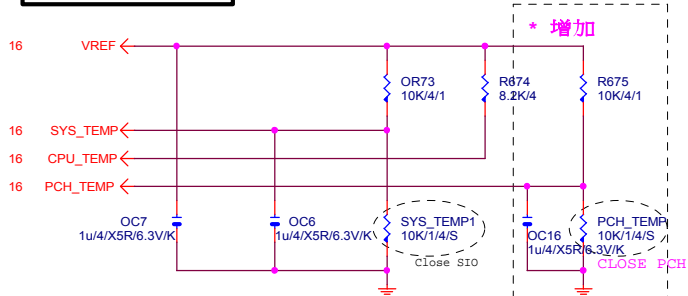
Diagram illustrating the 3VDD1 power plane connections. The diagram shows various power pins and their connections to different voltage rails (VCC3, 3VDUAL PCH, 3VDD1).

- VCC3 Connections (Blue lines):**
 - PCIRSTIN (8.2K/4) to VCC3
 - N_LDRQ0 (1K/4/1) to VCC3
 - ITE_PWROK (1K/4/1) to VCC3
 - PROCHOT_CON (8.2K/4/X) to VCC3
- 3VDUAL PCH Connections (Green lines):**
 - GP_GP17 (8.2K/4) to 3VDUAL PCH
 - BIOS_SEL (8.2K/4) to 3VDUAL PCH
- 3VDD1 Connections (Red lines):**
 - IO_GP3 (8.2K/4) to VCC3
 - N_A20GATE (8.2K/4) to 3VDD1

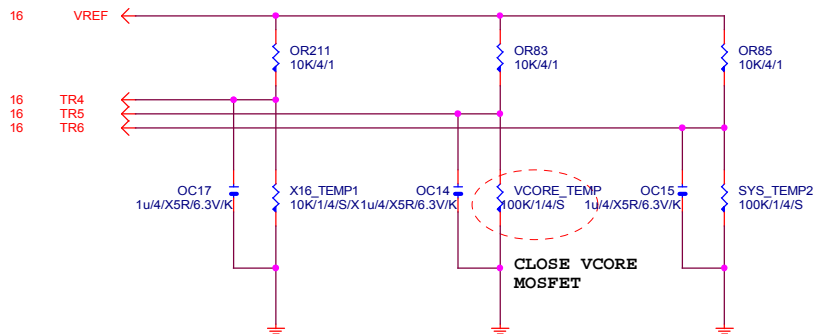
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Size	Document Number	GA-B250M-EVO		Rev	
C				1.	
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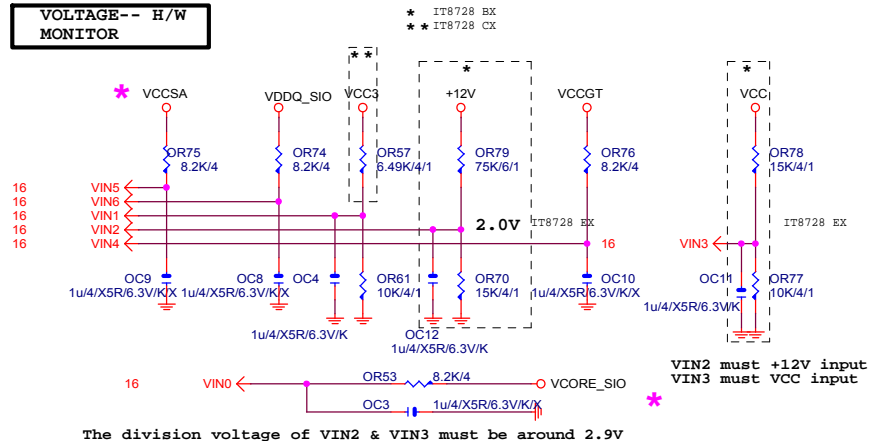
TEMP H/W MONITOR



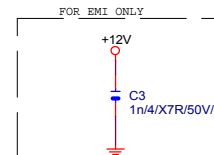
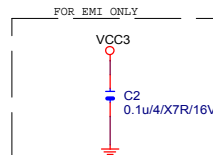
5個FAN時使用



VOLTAGE-- H/W MONITOR



★Update 2015.05.27

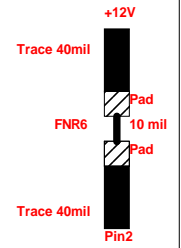
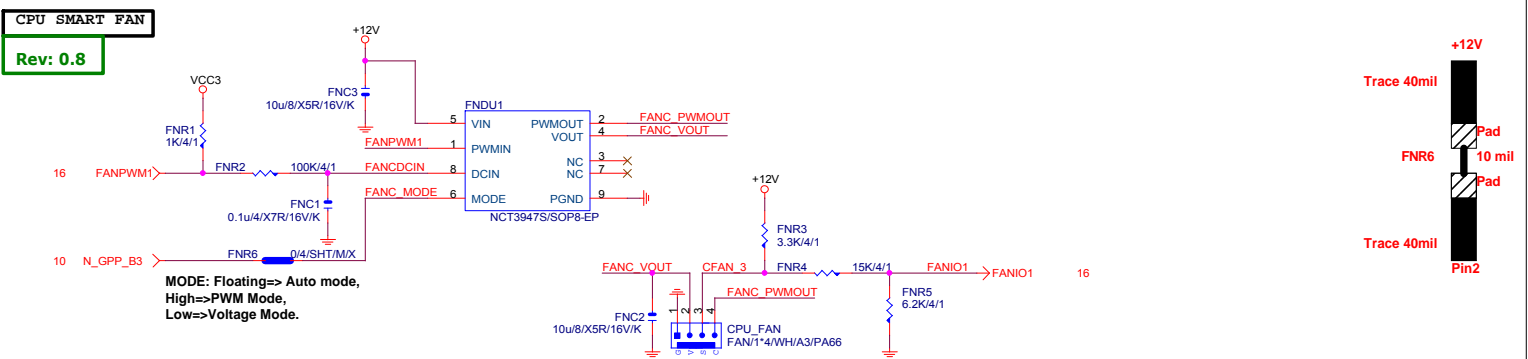


Gigabyte Technology

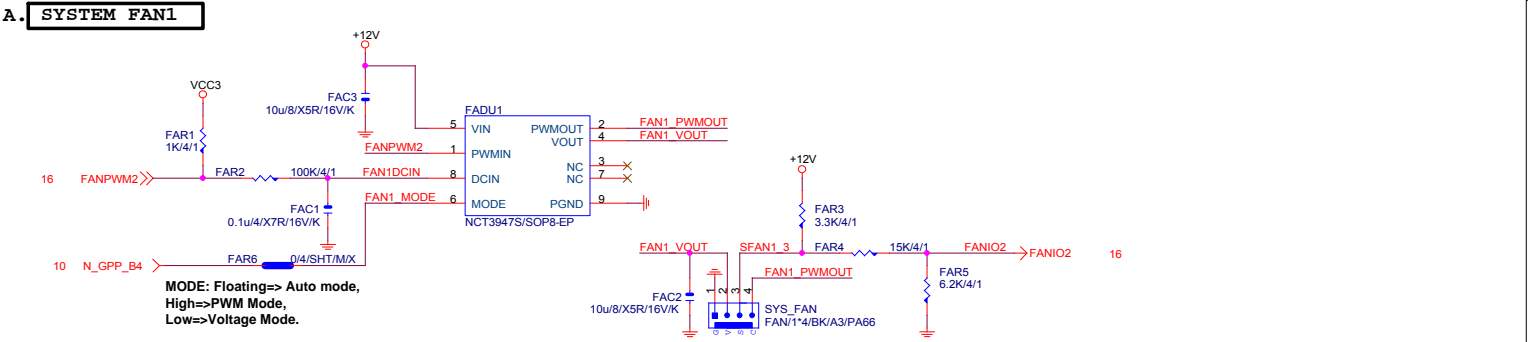
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Size	Document Number	Rev	
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CPU SMART FAN

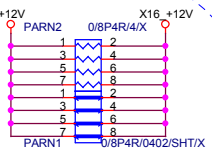
Rev: 0.8



A. SYSTEM FAN1



Rev 0.2

+12V_protect
short-wire test

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 PA_EXP_RXN0_15] >> PA_EXP_RXN[0..15] 4
 PA_EXP_TXP0_15] >> PA_EXP_TXP[0..15] 4
 PA_EXP_TXN0_15] >> PA_EXP_TXN[0..15] 4

PA_EXP_TXP0	PAC5	0.22u4/X5R/6.3V/K	PA_EXP_TXP0 C
PA_EXP_TXN0	PAC4	0.22u4/X5R/6.3V/K	PA_EXP_TXN0 C
PA_EXP_TXP1	PAC6	0.22u4/X5R/6.3V/K	PA_EXP_TXP1 C
PA_EXP_TXN1	PAC7	0.22u4/X5R/6.3V/K	PA_EXP_TXN1 C
PA_EXP_TXP2	PAC8	0.22u4/X5R/6.3V/K	PA_EXP_TXP2 C
PA_EXP_TXN2	PAC9	0.22u4/X5R/6.3V/K	PA_EXP_TXN2 C
PA_EXP_TXP3	PAC10	0.22u4/X5R/6.3V/K	PA_EXP_TXP3 C
PA_EXP_TXN3	PAC11	0.22u4/X5R/6.3V/K	PA_EXP_TXN3 C
PA_EXP_TXP4	PAC12	0.22u4/X5R/6.3V/K	PA_EXP_TXP4 C
PA_EXP_TXN4	PAC13	0.22u4/X5R/6.3V/K	PA_EXP_TXN4 C
PA_EXP_TXP5	PAC14	0.22u4/X5R/6.3V/K	PA_EXP_TXP5 C
PA_EXP_TXN5	PAC15	0.22u4/X5R/6.3V/K	PA_EXP_TXN5 C
PA_EXP_TXP6	PAC16	0.22u4/X5R/6.3V/K	PA_EXP_TXP6 C
PA_EXP_TXN6	PAC17	0.22u4/X5R/6.3V/K	PA_EXP_TXN6 C
PA_EXP_TXP7	PAC18	0.22u4/X5R/6.3V/K	PA_EXP_TXP7 C
PA_EXP_TXN7	PAC19	0.22u4/X5R/6.3V/K	PA_EXP_TXN7 C
PA_EXP_TXP8	PAC21	0.22u4/X5R/6.3V/K	PA_EXP_TXP8 C
PA_EXP_TXN8	PAC20	0.22u4/X5R/6.3V/K	PA_EXP_TXN8 C
PA_EXP_TXP9	PAC22	0.22u4/X5R/6.3V/K	PA_EXP_TXP9 C
PA_EXP_TXN9	PAC23	0.22u4/X5R/6.3V/K	PA_EXP_TXN9 C
PA_EXP_TXP10	PAC24	0.22u4/X5R/6.3V/K	PA_EXP_TXP10 C
PA_EXP_TXN10	PAC25	0.22u4/X5R/6.3V/K	PA_EXP_TXN10 C
PA_EXP_TXP11	PAC26	0.22u4/X5R/6.3V/K	PA_EXP_TXP11 C
PA_EXP_TXN11	PAC27	0.22u4/X5R/6.3V/K	PA_EXP_TXN11 C
PA_EXP_TXP12	PAC28	0.22u4/X5R/6.3V/K	PA_EXP_TXP12 C
PA_EXP_TXN12	PAC29	0.22u4/X5R/6.3V/K	PA_EXP_TXN12 C
PA_EXP_TXP13	PAC30	0.22u4/X5R/6.3V/K	PA_EXP_TXP13 C
PA_EXP_TXN13	PAC31	0.22u4/X5R/6.3V/K	PA_EXP_TXN13 C
PA_EXP_TXP14	PAC32	0.22u4/X5R/6.3V/K	PA_EXP_TXP14 C
PA_EXP_TXN14	PAC33	0.22u4/X5R/6.3V/K	PA_EXP_TXN14 C
PA_EXP_TXP15	PAC34	0.22u4/X5R/6.3V/K	PA_EXP_TXP15 C
PA_EXP_TXN15	PAC35	0.22u4/X5R/6.3V/K	PA_EXP_TXN15 C

PCIEX16:16/5/5/5/16

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWIDTH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

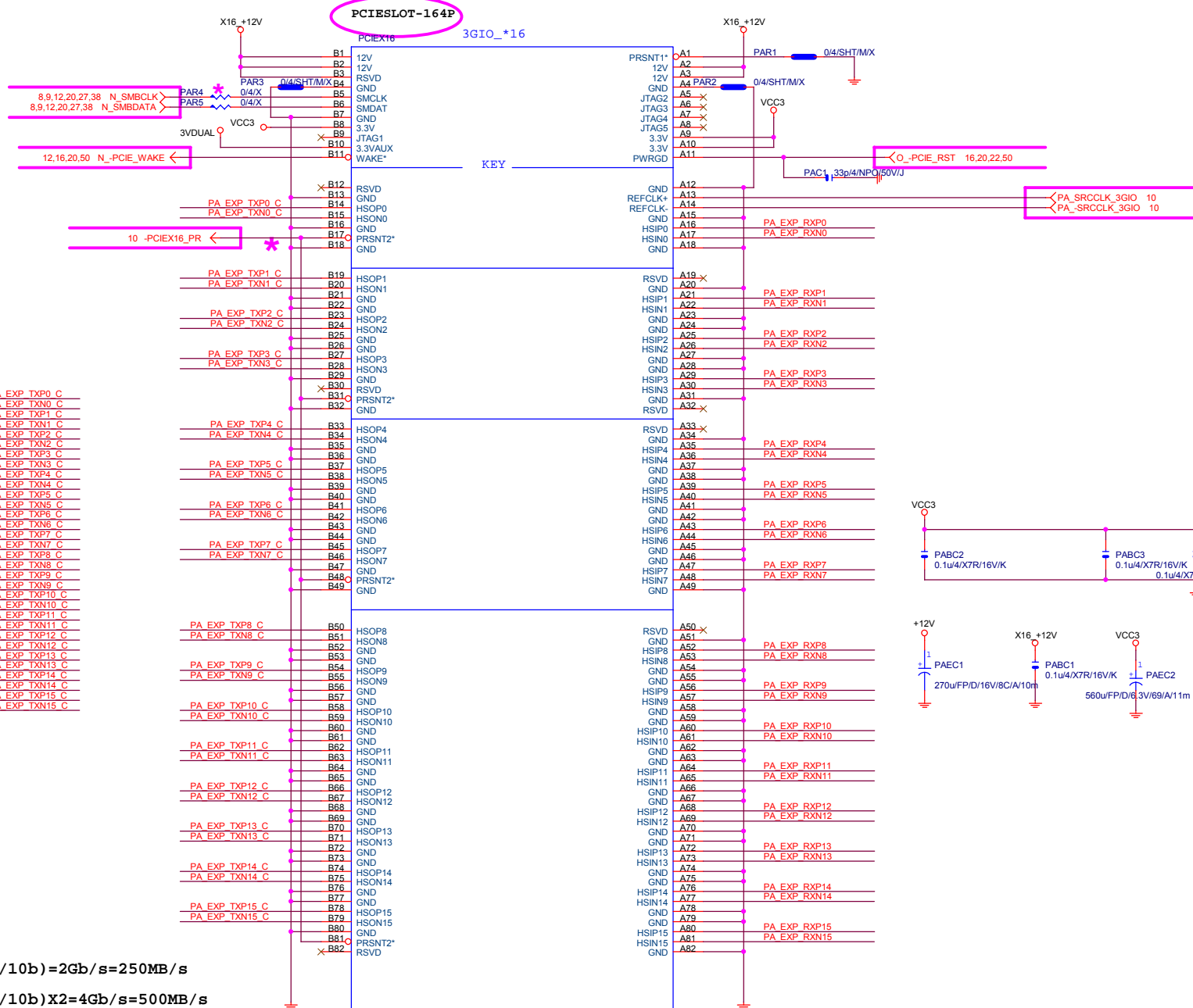
PCE-E X1(雙向) BANDWIDTH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWIDTH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWIDTH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

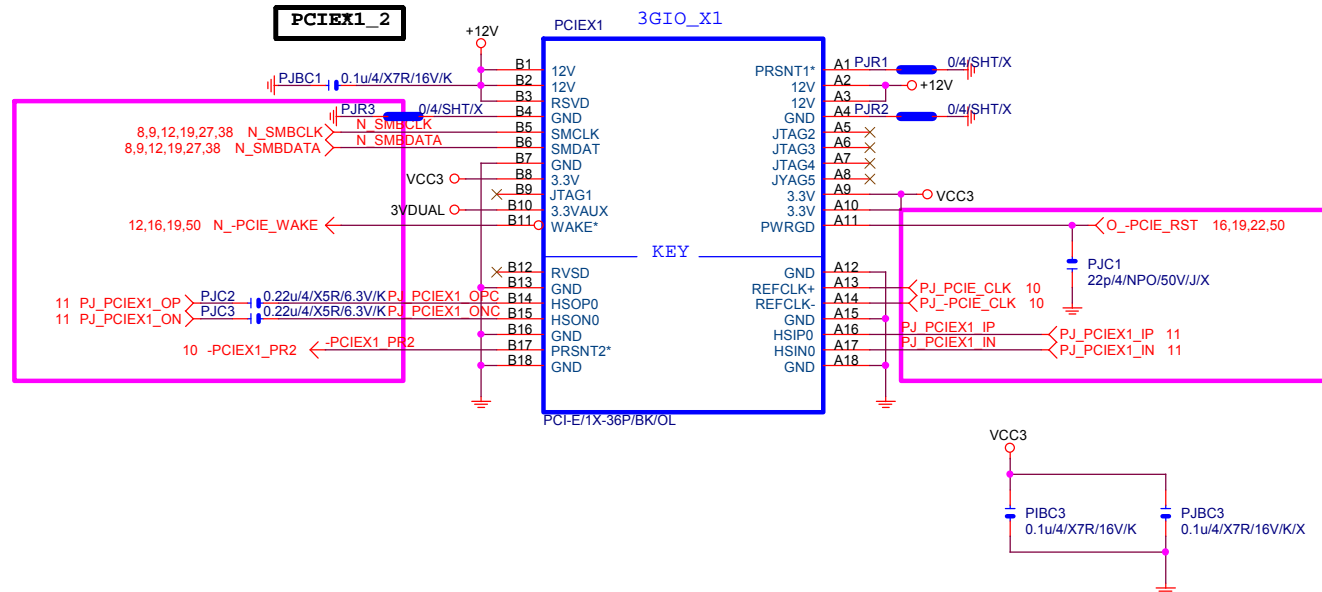
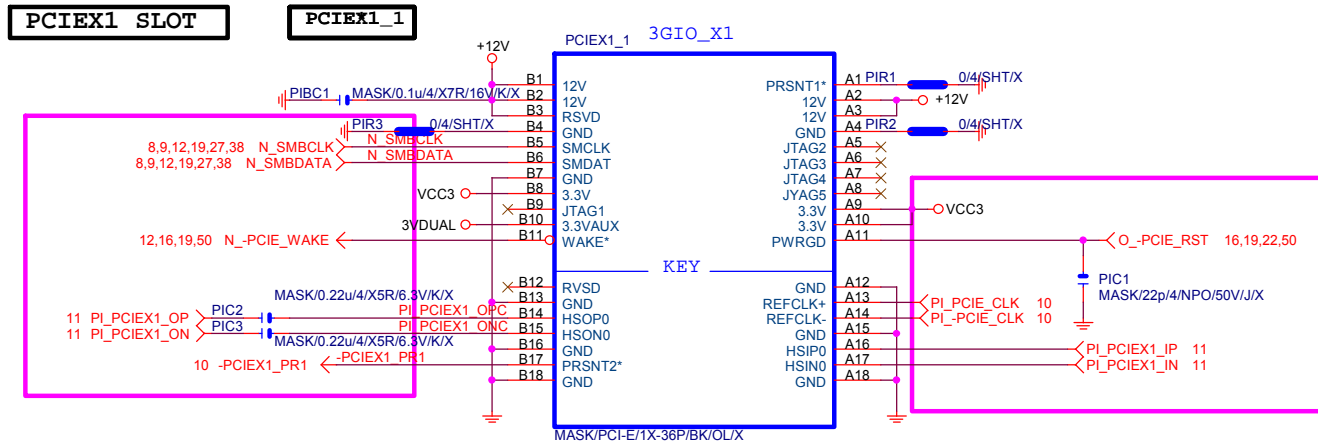
PCIESLOT-164P



PCI-E/16X-164P/BK/LONG DOUBLE

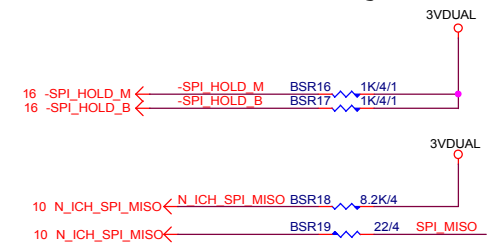
Gigabyte Technology

Title			PCI EXPRESS * 16	
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Title		
PCIE_X4		
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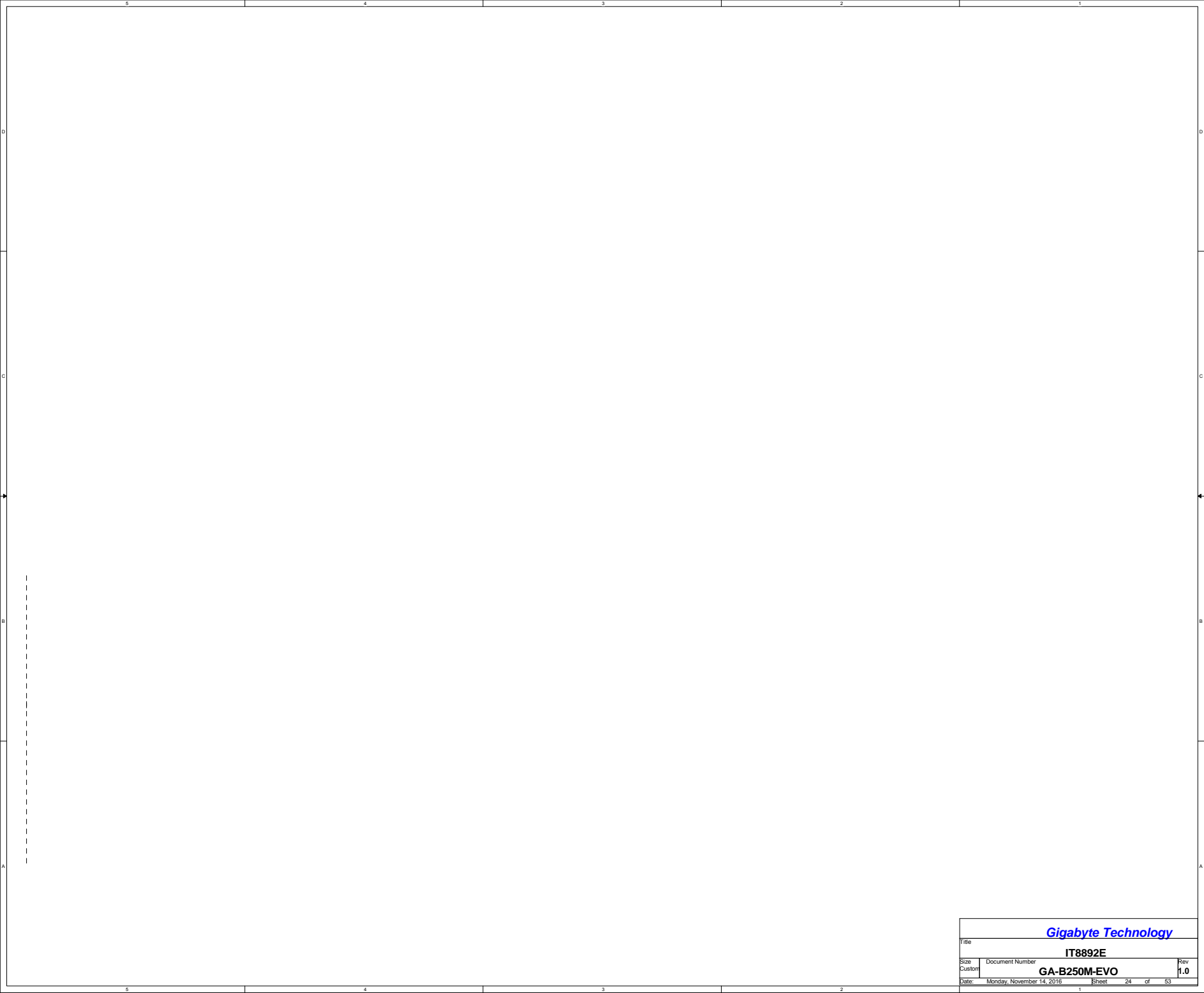
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

3VDUAL

BSC5
0.1u/4/X7R/16V/K/X



5					4					3					2					1																																																																															
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C																																																																																																			
B																																																																																																			
A																																																																																																			
										<table><tr><td colspan="15">Title</td></tr><tr><td colspan="15">Gigabyte Technology M.2X4_S4~S5 SWITCH</td></tr><tr><td>Size</td><td colspan="10">Document Number</td><td colspan="4">Rev</td></tr><tr><td>Custom</td><td colspan="10">GA-B250M-EVO</td><td colspan="4">1.0</td></tr><tr><td colspan="5">Date:</td><td colspan="5">Monday, November 14, 2016</td><td>Sheet</td><td>23</td><td>of</td><td>53</td><td></td></tr></table>															Title															Gigabyte Technology M.2X4_S4~S5 SWITCH															Size	Document Number										Rev				Custom	GA-B250M-EVO										1.0				Date:					Monday, November 14, 2016					Sheet	23	of	53	
Title																																																																																																			
Gigabyte Technology M.2X4_S4~S5 SWITCH																																																																																																			
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Custom	GA-B250M-EVO										1.0																																																																																								
Date:					Monday, November 14, 2016					Sheet	23	of	53																																																																																						
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Title		
IT8892E		
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Title

PCI SLOT 1&2

Size

Size	Custom
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Document Number

GA-B250M-EVO

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1.0

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53

Rev 0.1

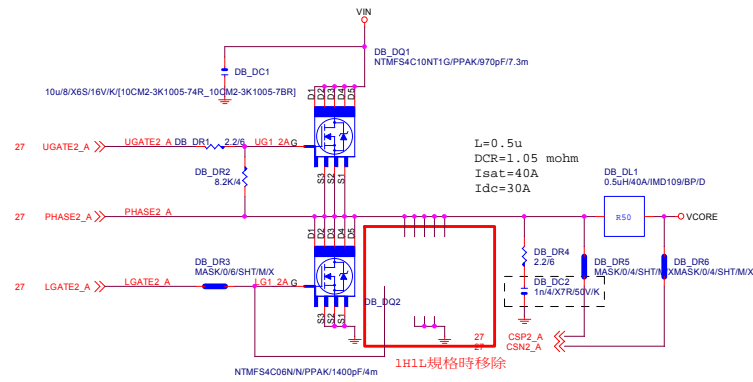
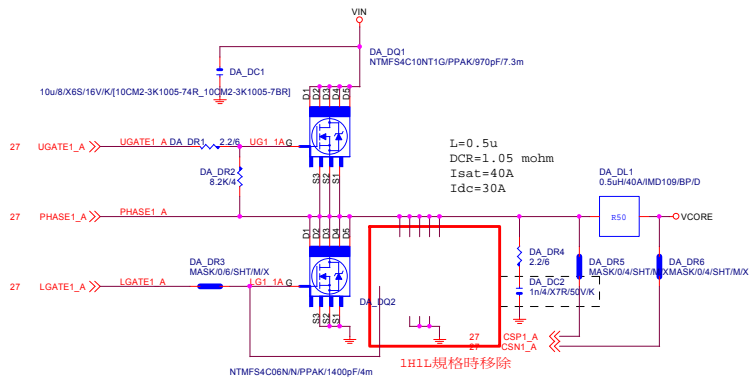
Gigabyte Technology

Title			
ASM1085 POWER			
Size	Document Number		Rev
Custom	GA-B250M-EVO		1.0
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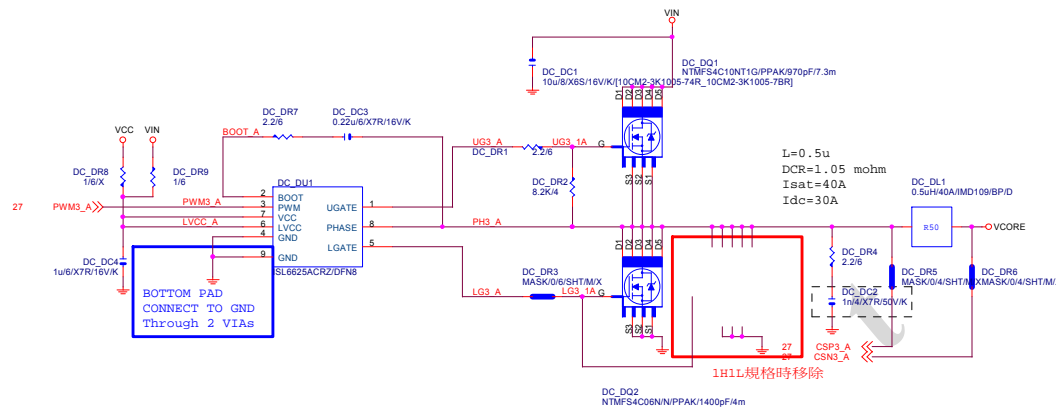
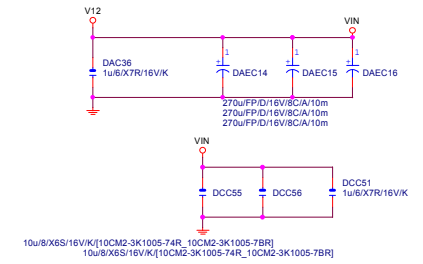
10 N_CPU_S \gg 90T23 連接至PCH:GPP_G15

REV:0.1

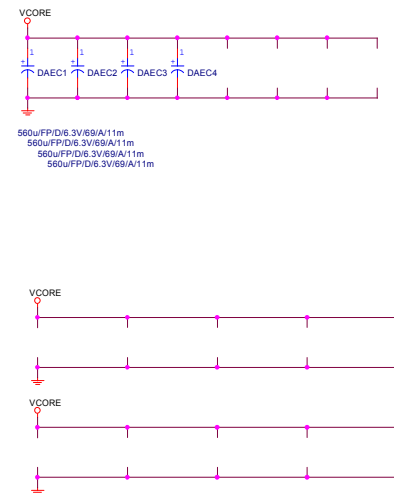
VCORE



VIN CAP 270u*3PCS



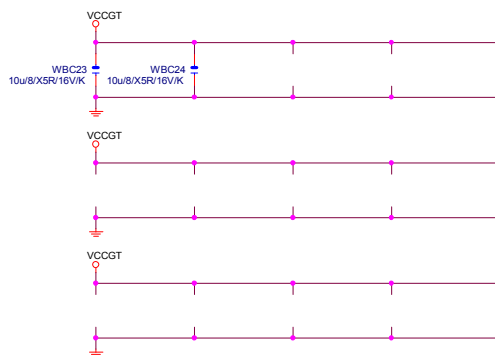
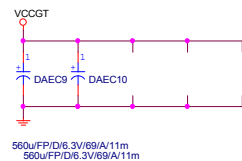
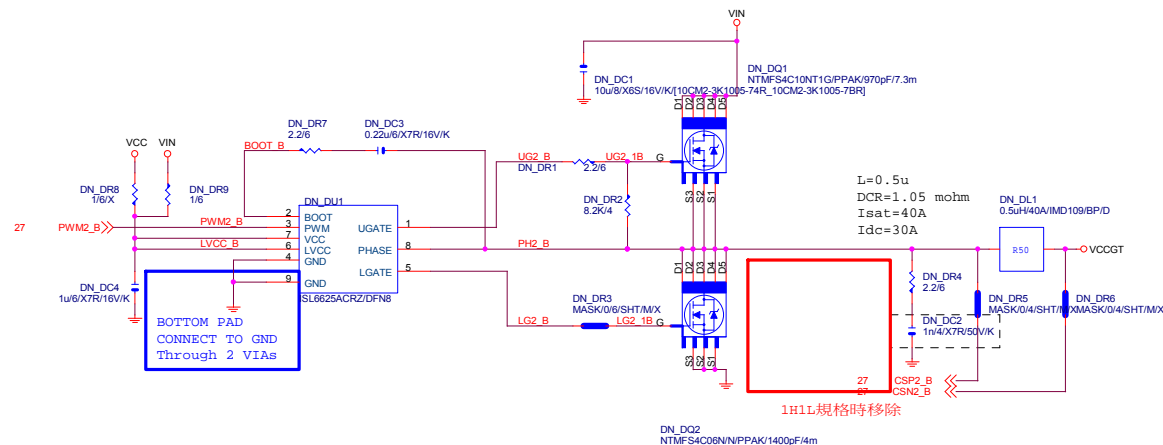
VCORE CAP 560u*4PCS 10u*10PCS



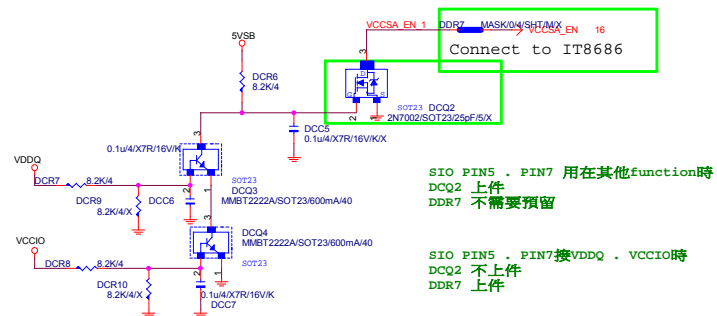
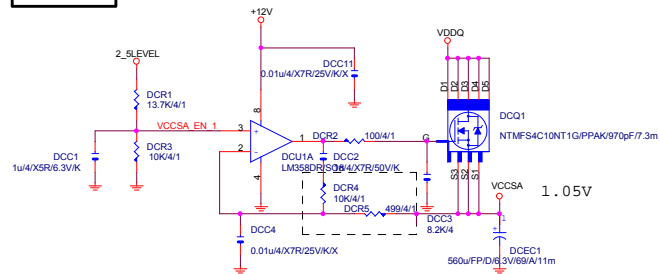
GIGABYTE™

Title			ISL95868_MOS
Size	Document Number	Rev	
	GA-B250M-EVO	1.0	
Date:	Monday, November 14, 2016	Sheet	26 of 53

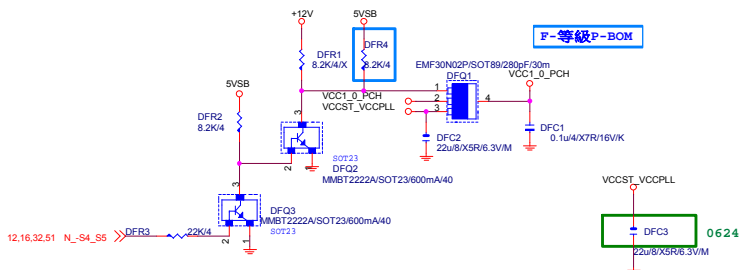
VCCGT CAP 560u*2PCS
10u*2PCS



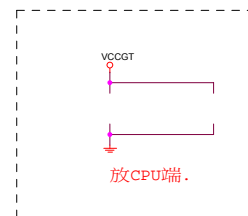
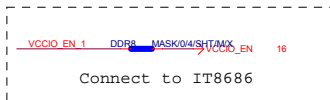
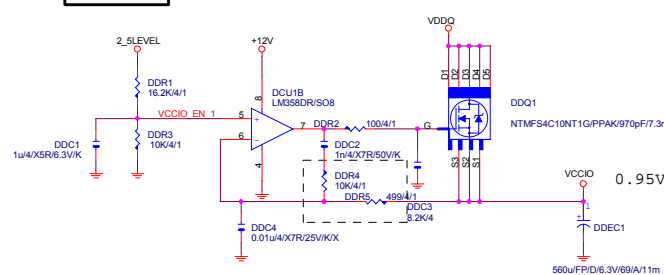
VCCSA



VCCST_VCCPLL



VCCIO



REV:0.2

DDR4

CHOKE與CAP料號可變

DDR VIN CAP
560u*2PCS

1.2V

SUPPORT DDR4

25A MAX

 $L=1\mu$
 $DCR=2.5\text{ m}\Omega$
 $I_{\text{sat}}=35\text{A}$
 $I_{\text{dc}}=28\text{A}$ 請放置CHOKE--出來位置,先預留.
請自行確認ripple後再決定是否上件

Remote sense請從最重的負載端點拉回

上件

MA_DR38.MA_DC15

VPP_25V使用8120.8068A.RT8237時上件

PWR SEQ

CLOSE TO DDR POWER PLANE

For power sequence require

VPP_25V使用8120時上件

MAU1上RT9045時上件(不可MASK)
MA VTT REF

DDRVTT

DDRVTT CAP

4 DDR_VTT_CTL MAR110 MASK/0/4/SHT/M/10/X_DDRVTT_EN
2,16,51 N_SLP_S3 MAR111 MASK/0/4/SHT/M/10/X_DDRVTT_BOOT

GIGABYTE™

Title
RT8237_DDR4 POWERSize
Custom
Document Number
GA-B250M-EVORev
1.0

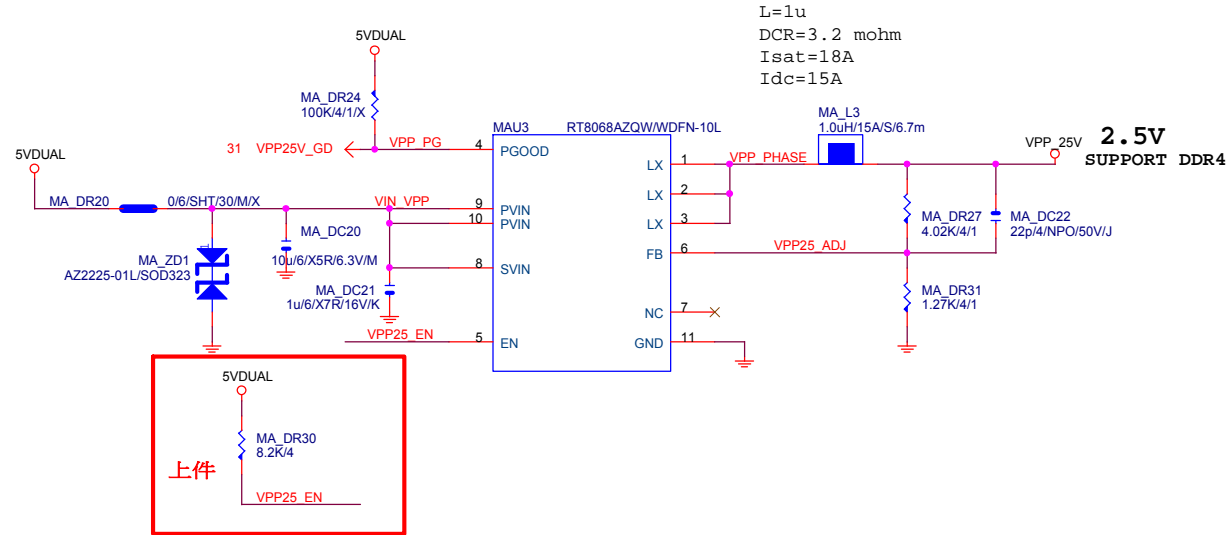
Date: Monday, November 14, 2016

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REV:0.1

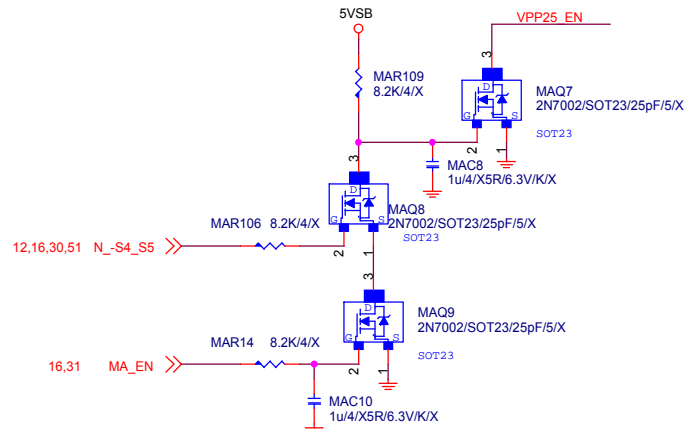
VPP_25V

CHOKE與CAP料號可變



PWR_SEQ

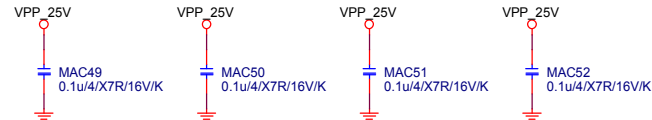
* 刪 MA_DR32



16 VPP25_EN_IO

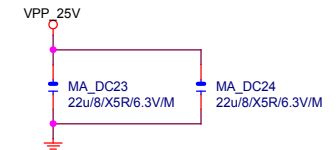
MAR114
MASK/0/4/SHT/M/X

VPP25_EN



VPP CAP 22u*1PCS

* 大電容 x0



GIGABYTE™

Title		
RT8068A_VPP25 POWER		
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Custom	GA-B250M-EVO	1.0
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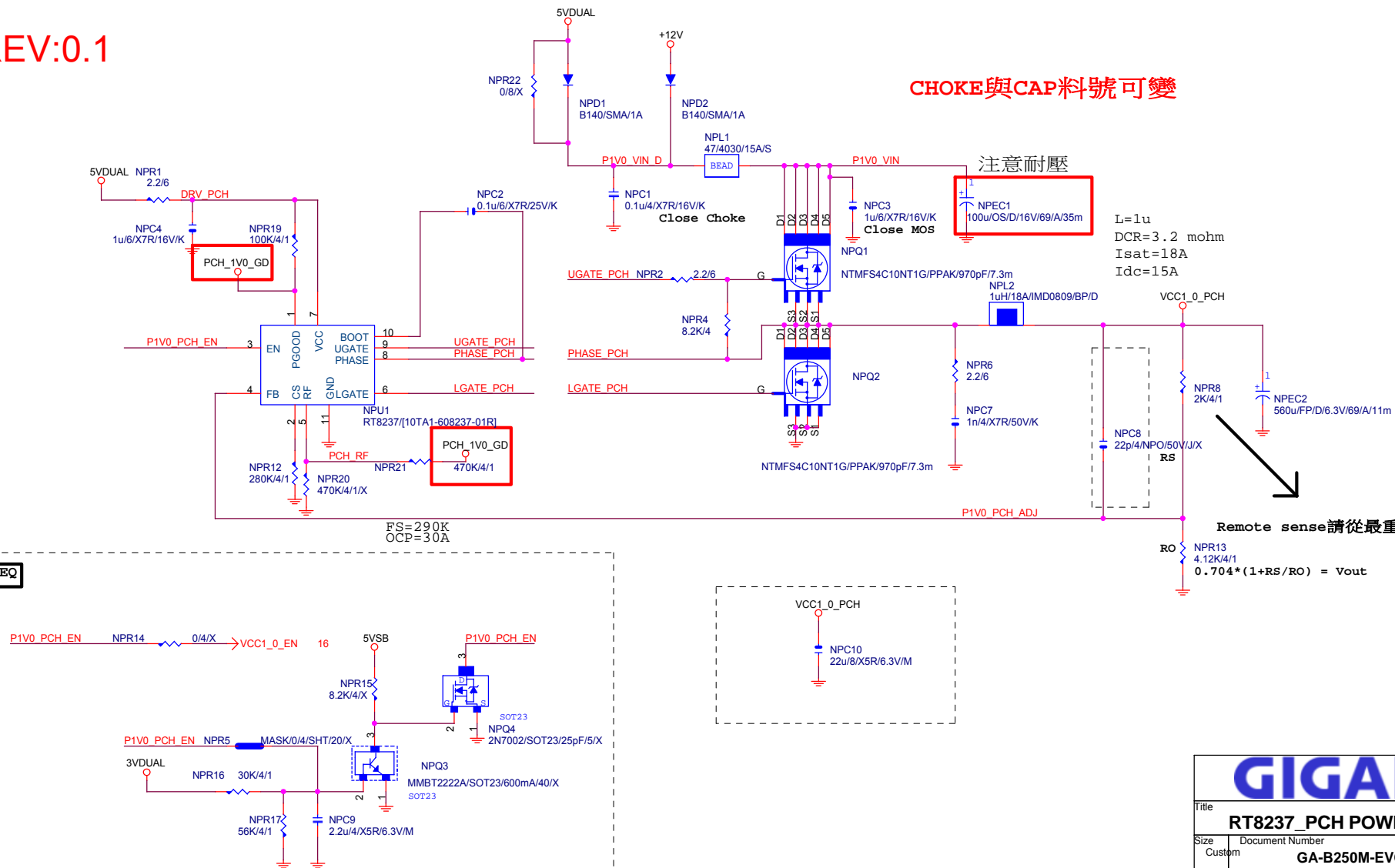
REV:0.1

CHOKE與CAP料號可變

注意耐壓

Remote sense請從最重的負載端點拉回

PWR_SEQ



GIGABYTE™

Title		
RT8237_PCH POWER		
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[illegible]

5VDUAL

3VDUAL

BC27 0.1u/4/X7R/16V/K

BC25 0.1u/4/X7R/16V/K

C9 22u/8/X5R/6.3V/M

C8 1n/4/X7R/50V/K

R37 100/4/1

R38 169/4/1

R36 22K/4

Q4 L1085DG/TO252/5A

O_RSMRST 12,16

上22u 电容

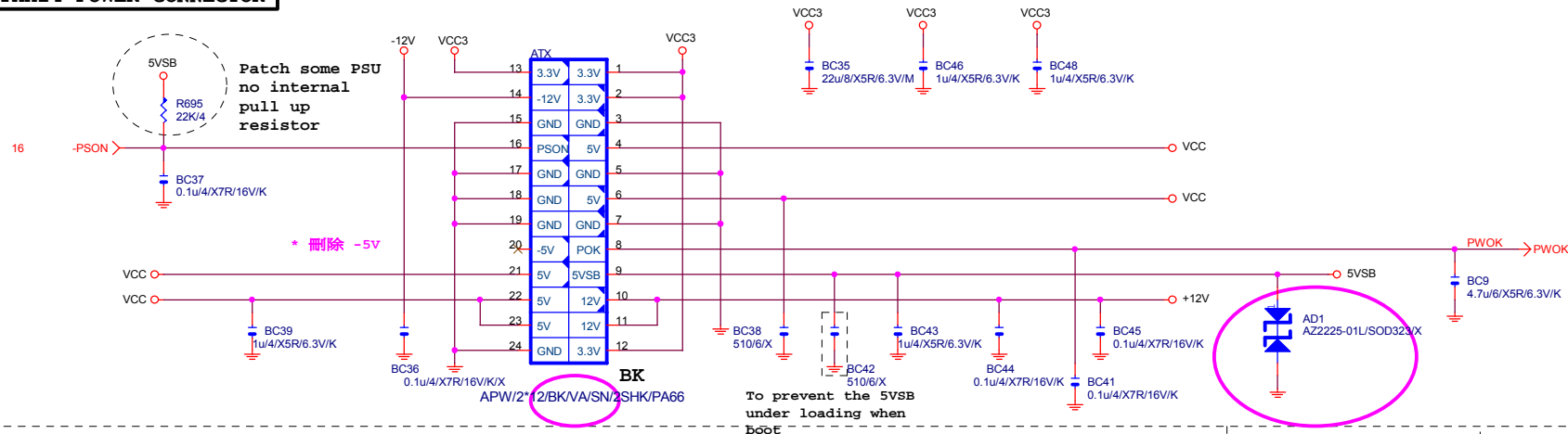
Meet the rise time

Rise:20% - 80%
Fall :2V- 0.8V

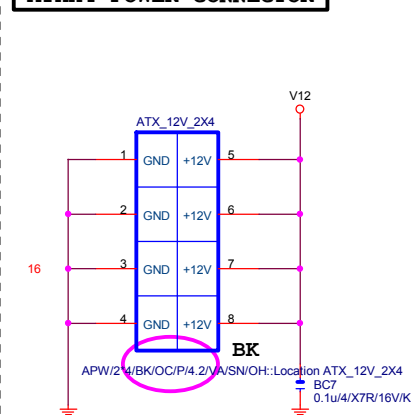
[illegible]

Title			
DISCRETE POWER			
Size	Document Number		Rev
Custom	GA-B250M-EVO		1.0
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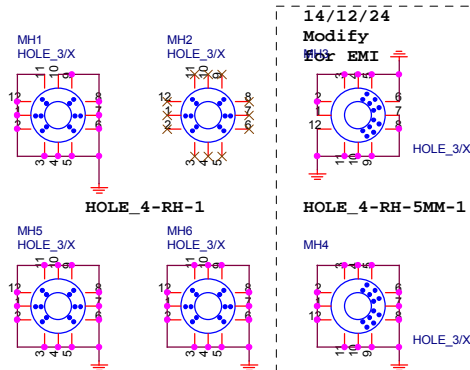
ATXX24 POWER CONNECTOR



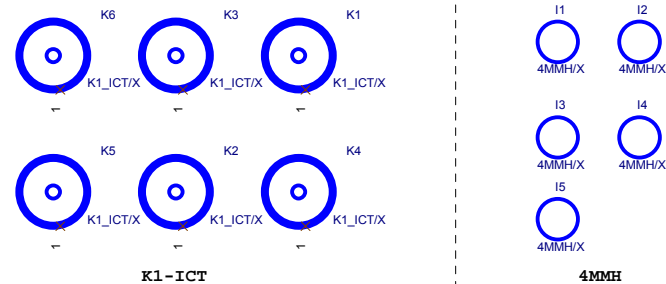
ATXX4 POWER CONNECTOR



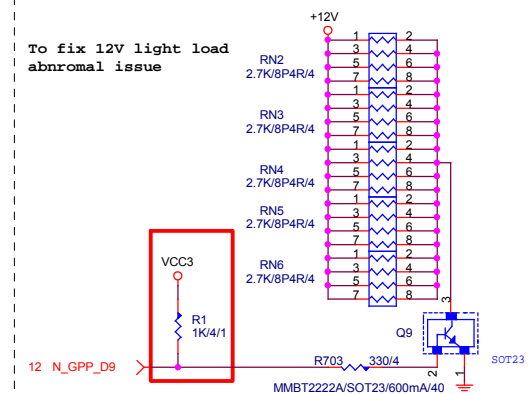
螺絲孔



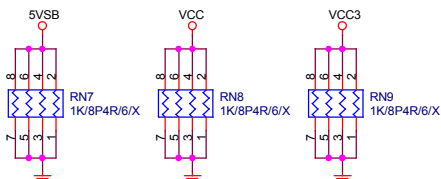
固定孔/光學點



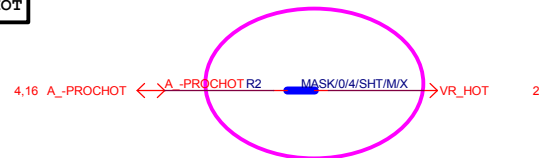
+12V DUMMY LOAD



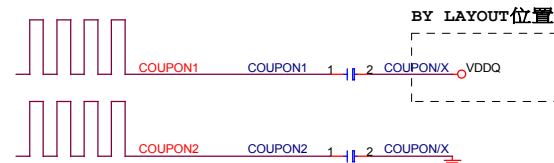
DUMMY LOAD



-PROHOT

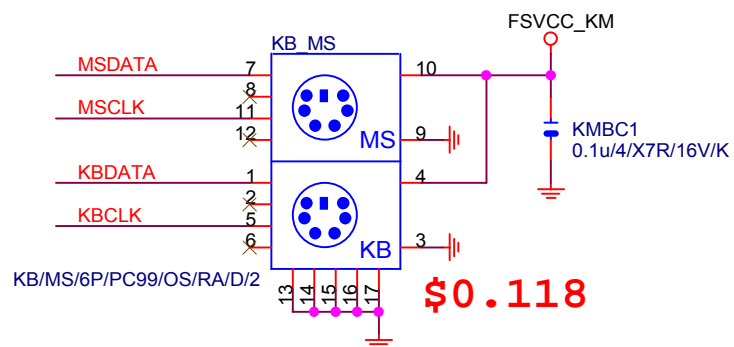


COUPON

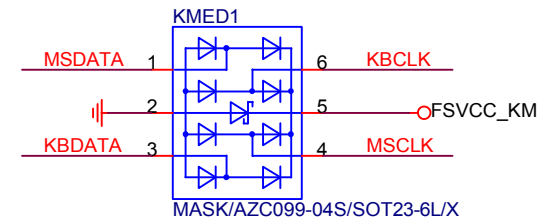


KB_MS_USB

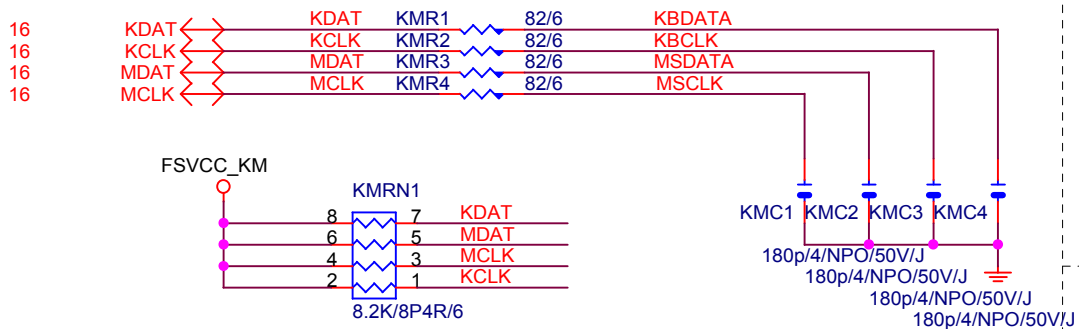
Rev: 81



ESD



KB_MS_USB DAMPING/PU



KB_MS_USB PWR

USB OC PROTECT

Gigabyte Technology

Title

KB_MS_USB

Size
A

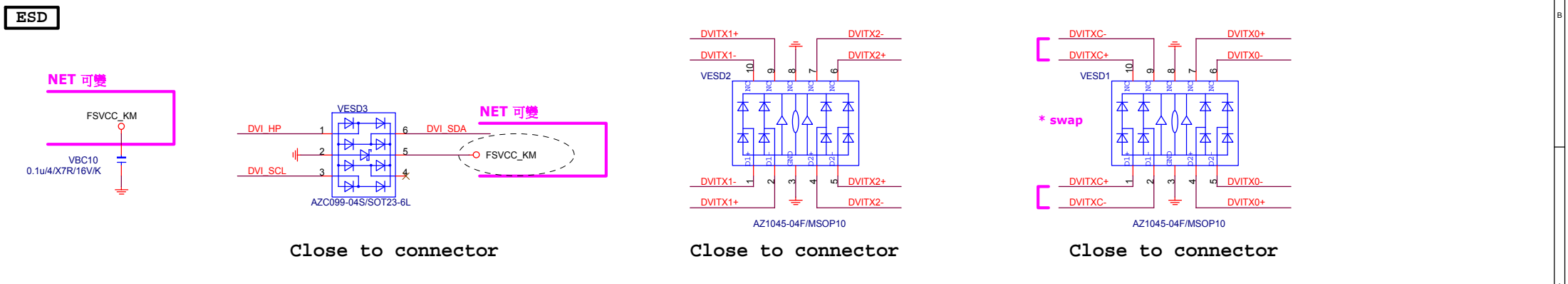
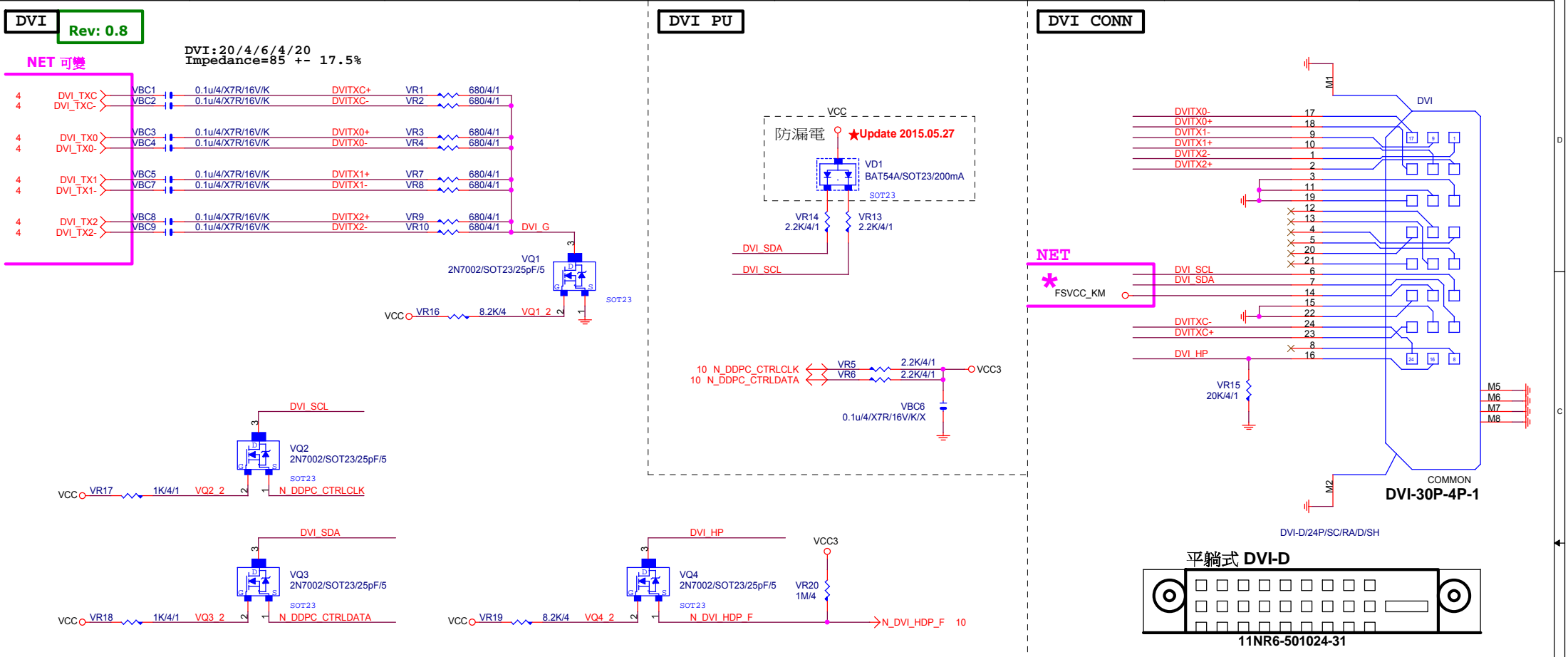
Document Number

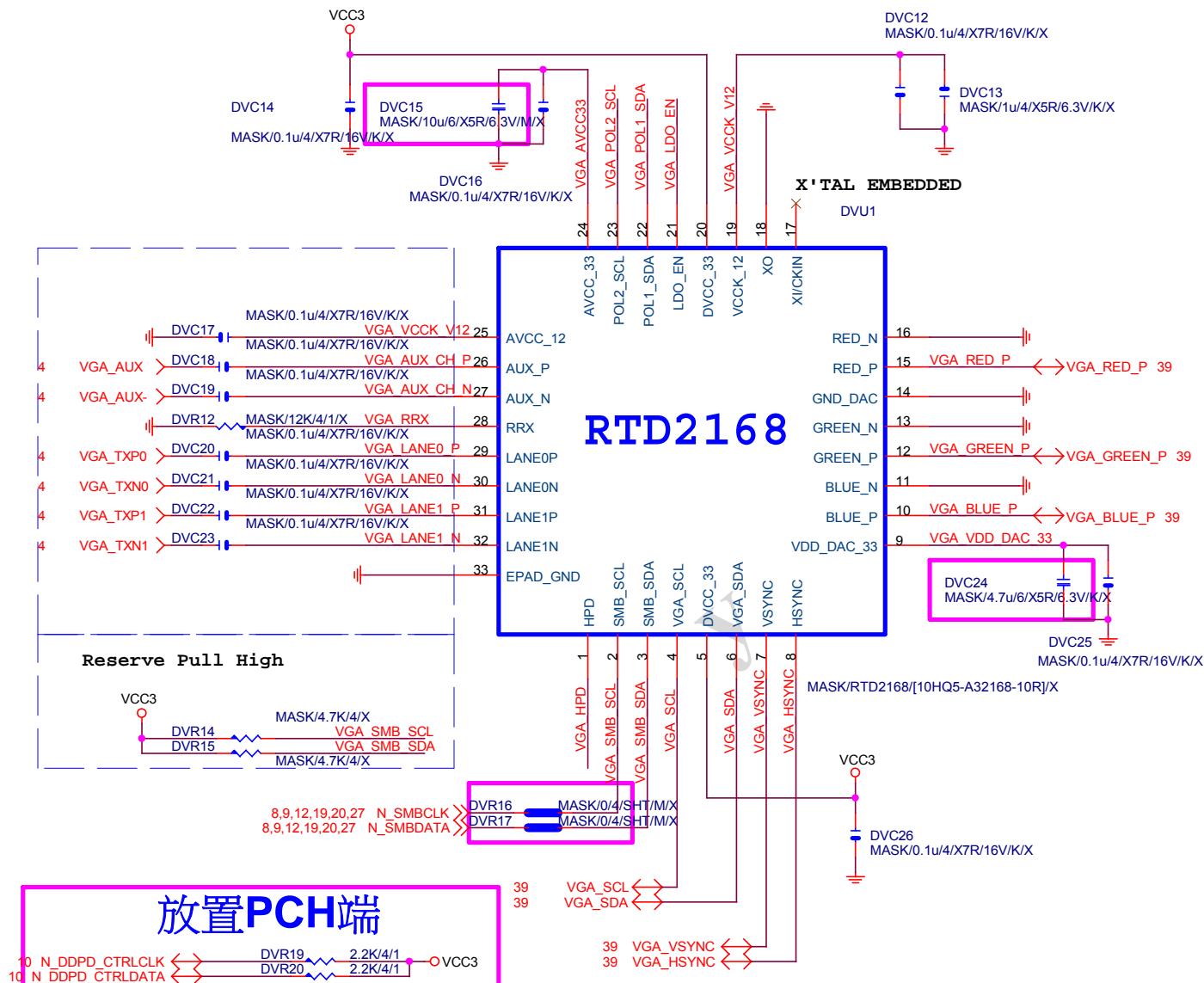
GA-B250M-EVO

Rev
1.0

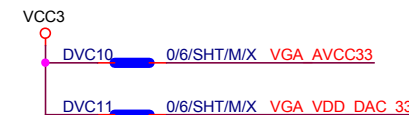
Date: Monday, November 14, 2016

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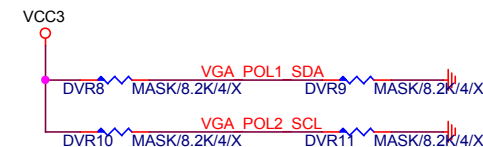




POWER



Power on latch



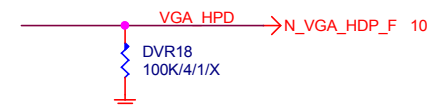
		POL1_SDA(PIN22)	
		0	1
POL2_SCL (PIN23)	0	X	EP MODE
	1	ROM ONLY MODE	EEPROM MODE

Embedded LDO

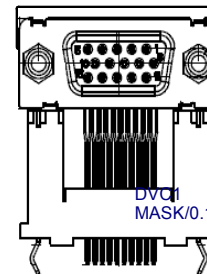
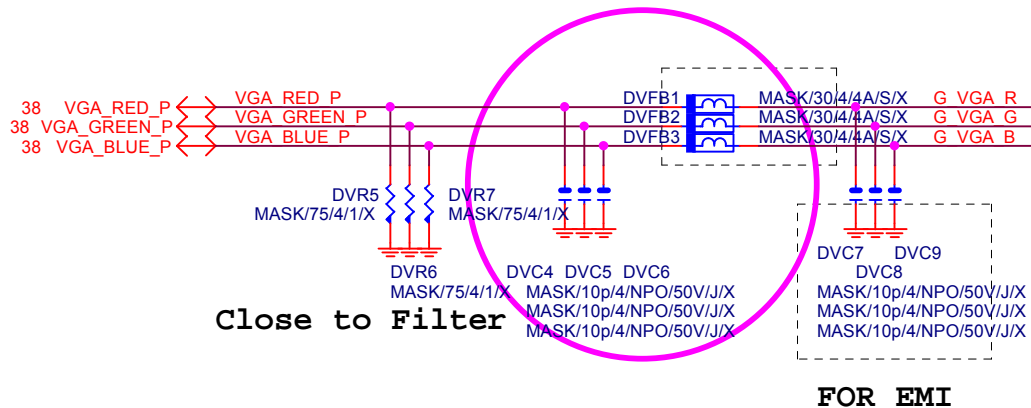
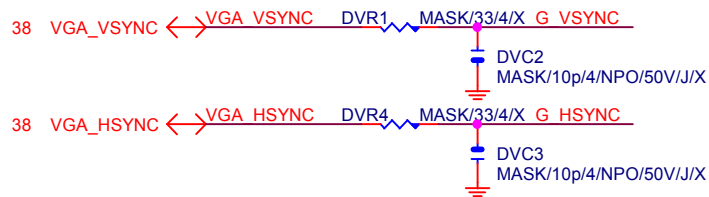
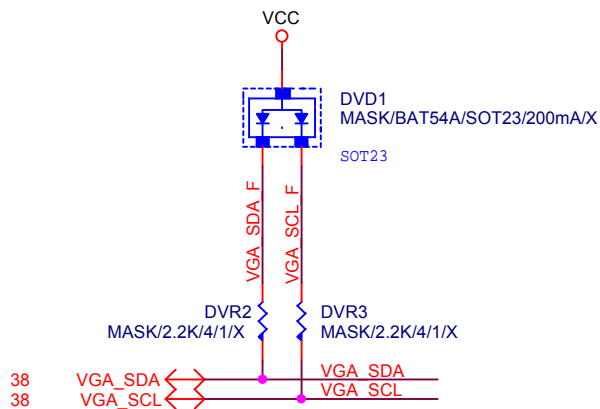


LDO_EN(PIN21)	
0	1
VCCK_V12 from External 1.2V	VCCK_V12 from Embedded LDO

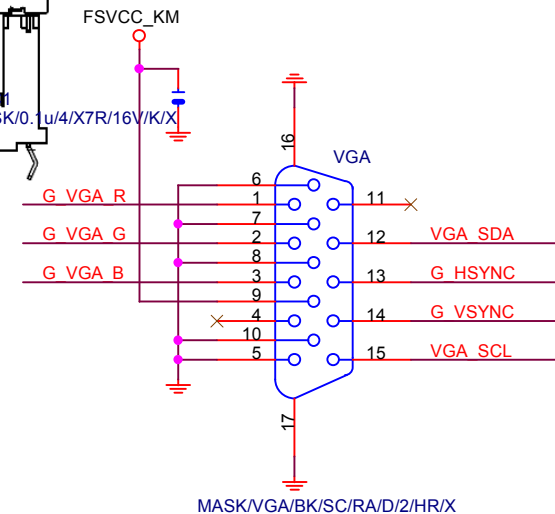
DP HPD



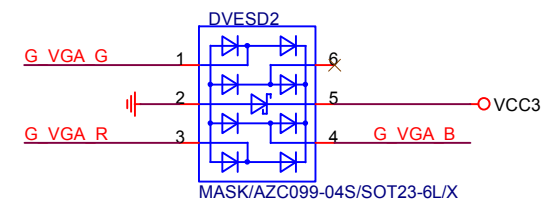
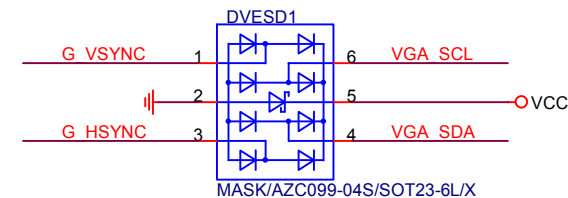
VGA SIGNAL R1.03



架高型VGA (BLACK)

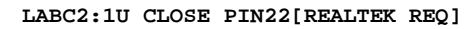
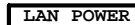


VGA ESD

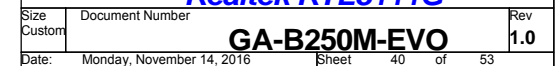
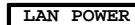
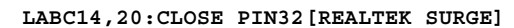
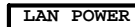


Gigabyte Technology
DP-VGA RTD2168

Title		
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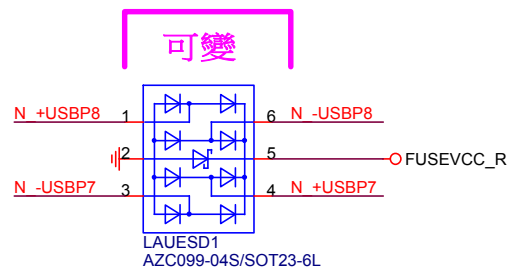


(CLOSE LAU1 PIN23)

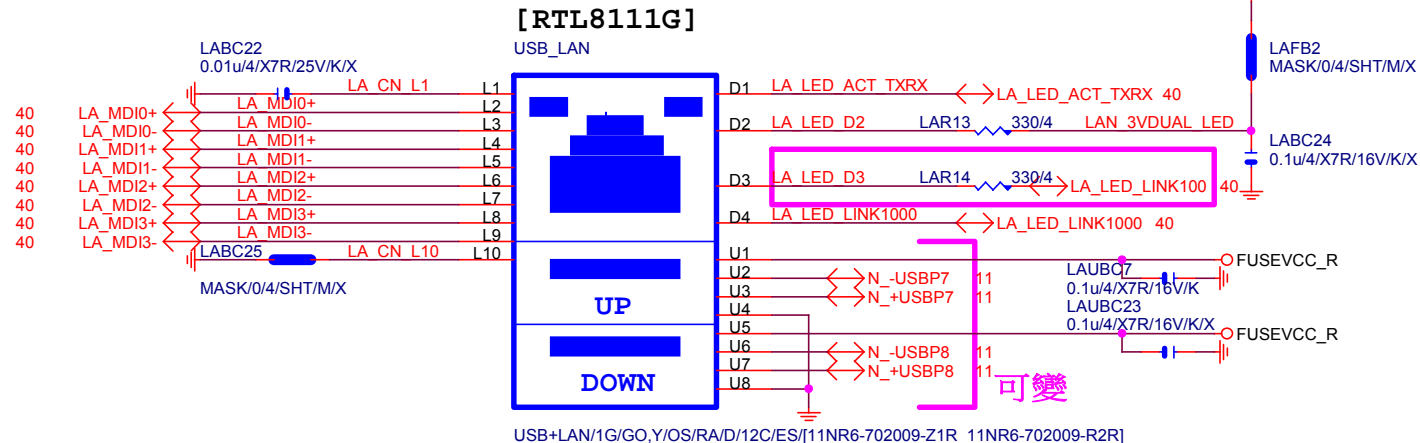


R1.06

note:可變更USB NAME

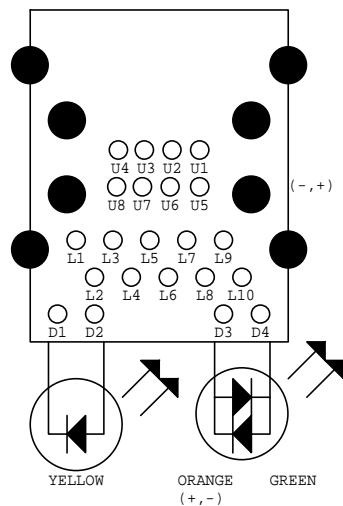


note:可變更USB NAME

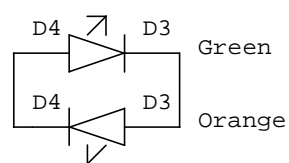


LA MDI -->100 : [20/4/8/4/20]

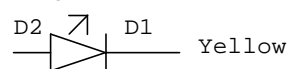
示意圖



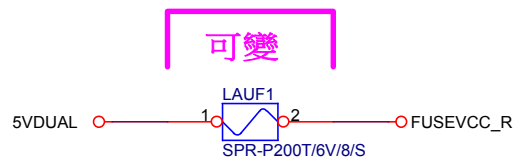
Dual Color LED



Single Color LED



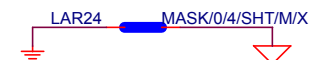
note:可變更FUSE



Close to connector
USB_LAN 2-Port 2.0A
FUSE-0805

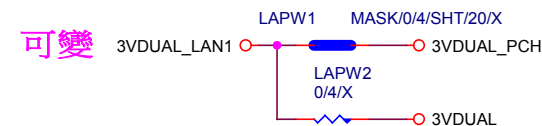
EMI SHORT PAD

PS:視EMI需求



LAN POWER

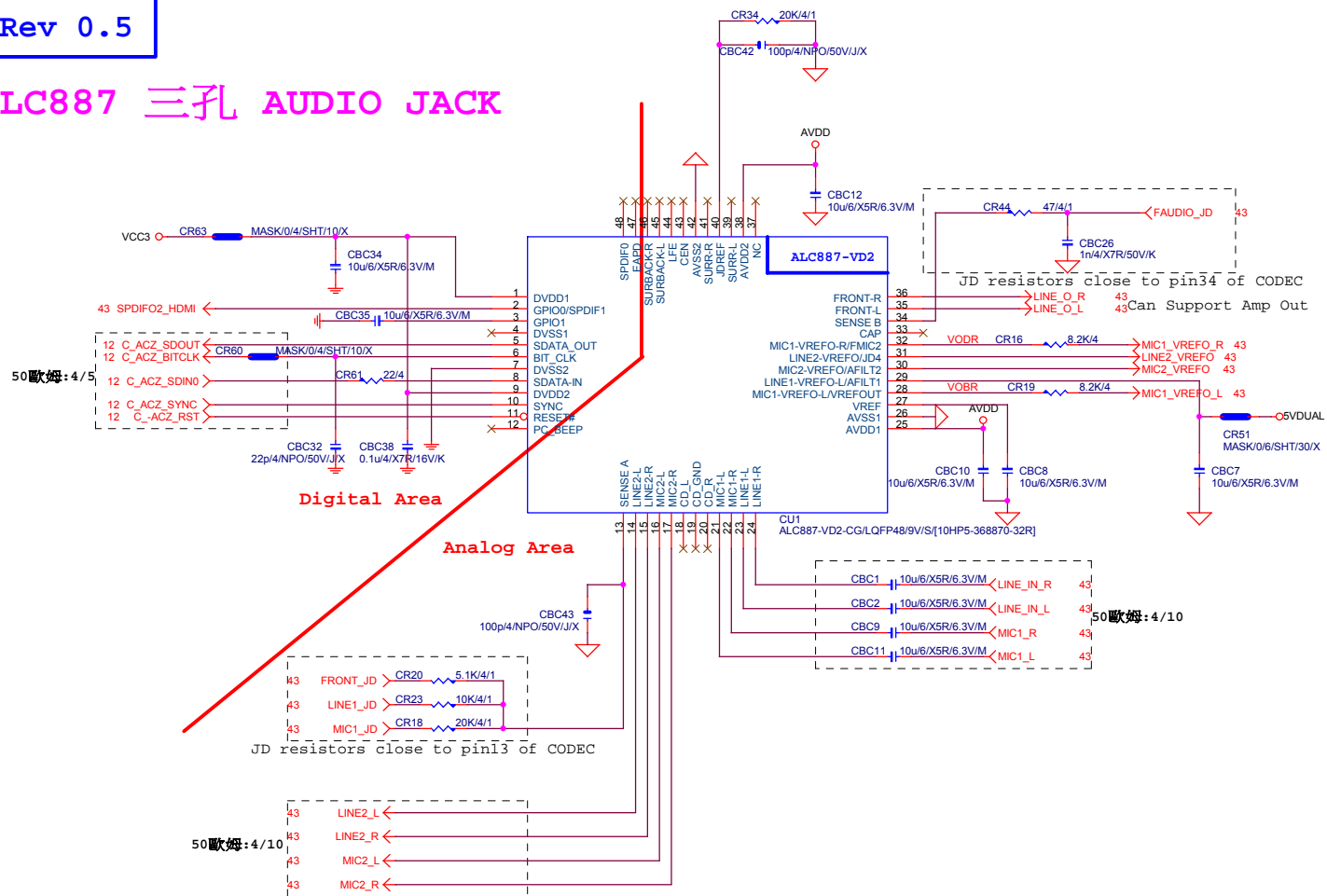
note: lan power連接及電流



Gigabyte Technology

LAN CONNECTOR-RTL8111HS

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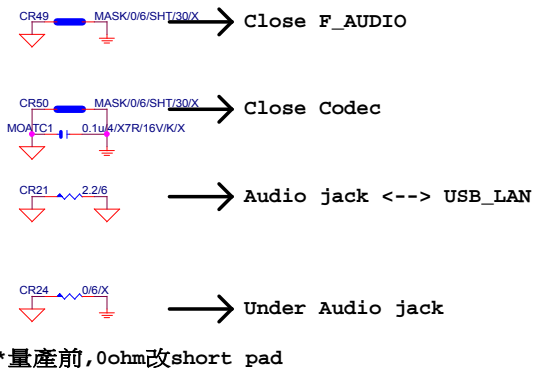
1. MH1空間夠,下DGND
空間不夠,改為Isolate
2. MH2一律改為Isolate

☐ MH1 MH2 ☐
 DGND Isolate

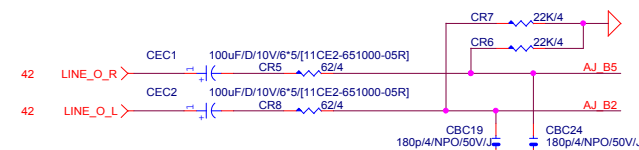
LAYOUT注意:要加
GND切割線



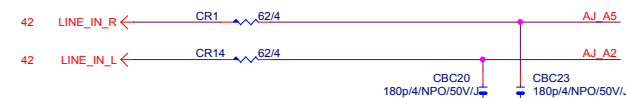
Rev 0.5



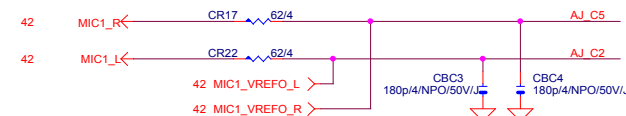
LINE-OUT



LINE-IN



MIC-IN

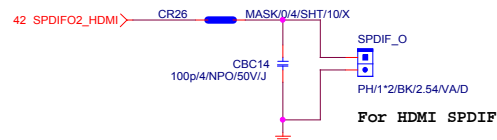


SURROUND

CEN/LFE

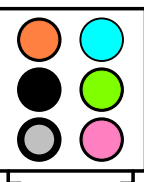
SURRBACK

SPDIF_OUT



SPDIF_IN

AZALIA JACK



AZALIA JACK

BLUE

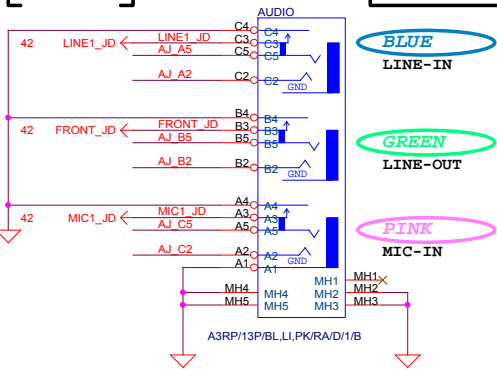
LINE-IN

GREEN

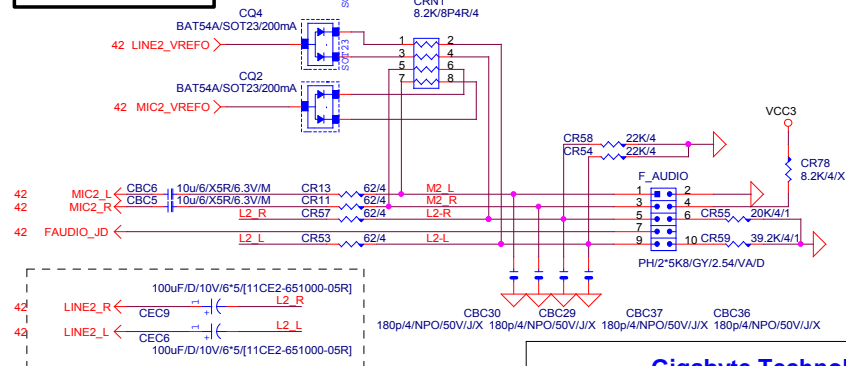
LINE-OUT

PINK

MIC-IN

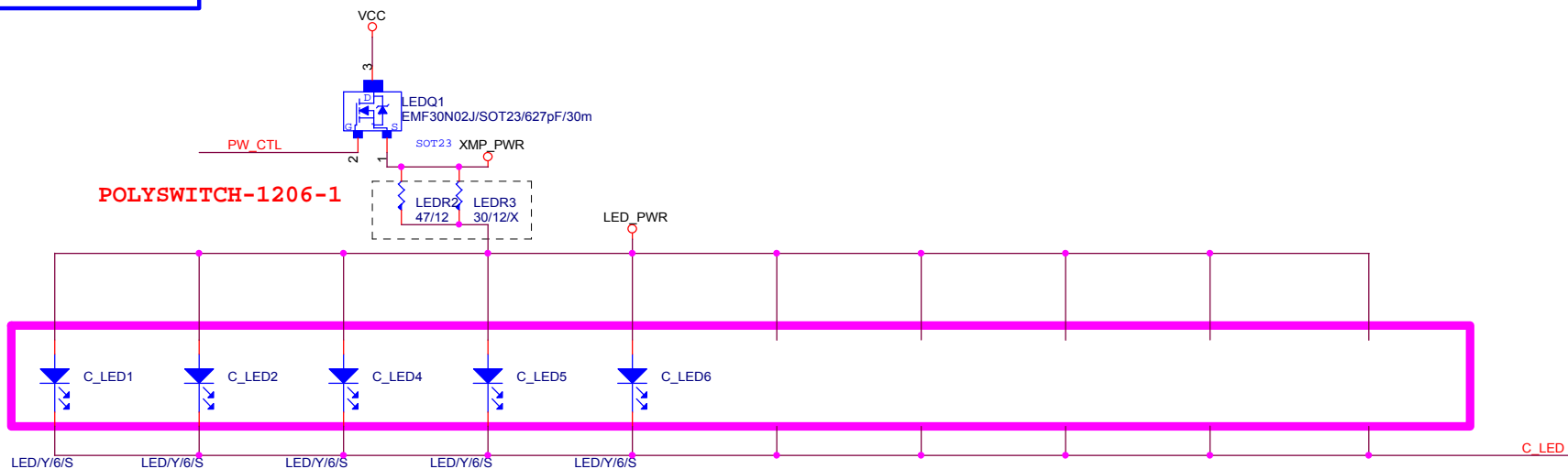


AZALIA FRONT PANEL



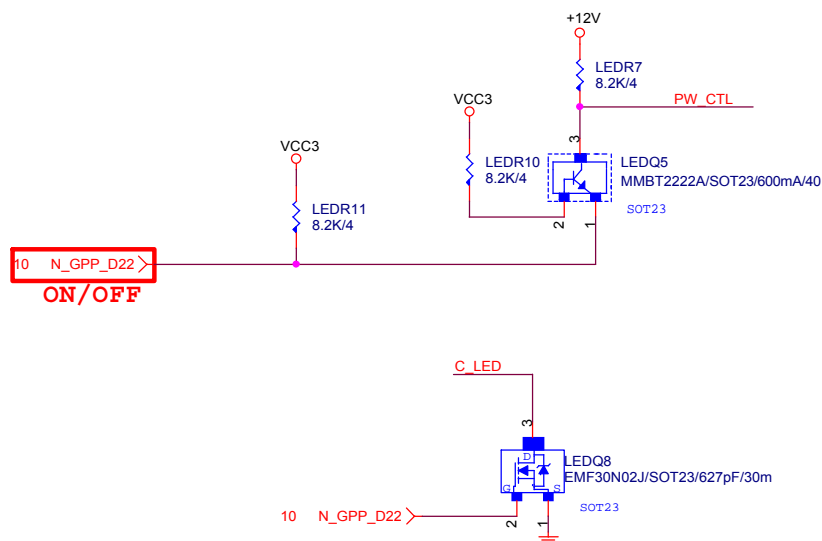
Gigabyte Technology

Title				Rev 1.0
AUDIO JACK				
Size Custom	Document Number		GA-B250M-EVO	
Date:	Monday, November 14, 2016	Sheet	43 of 53	



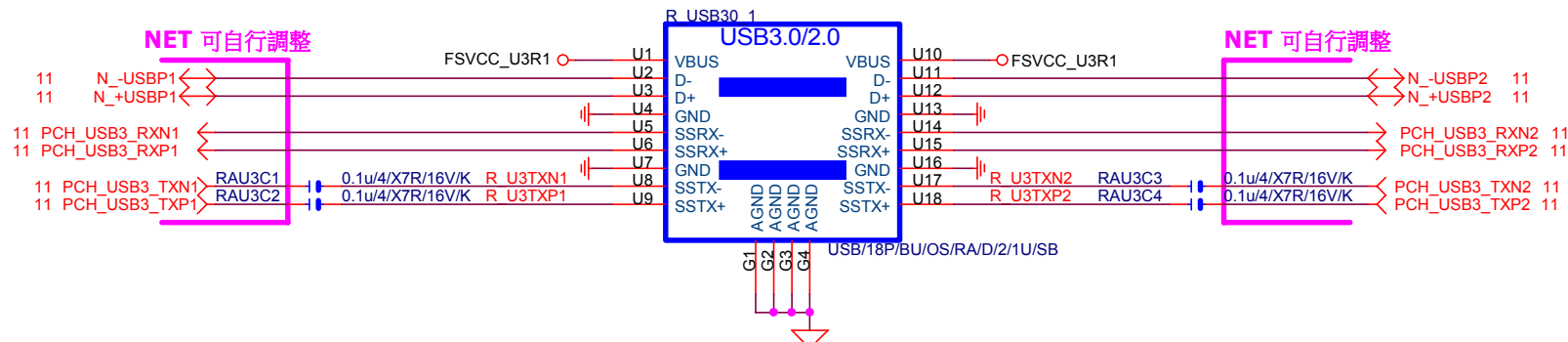
Ambient LED Control

	N_GPP_D22
Full Mode	H
OFF Mode	L



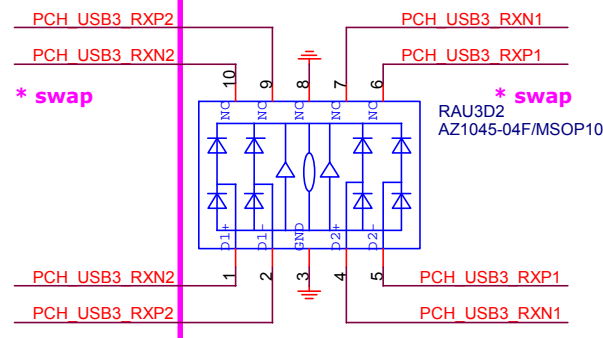
Rev: 0.53

ESD 可自行SWAP PIN ,CONN端 NET 名稱 不可

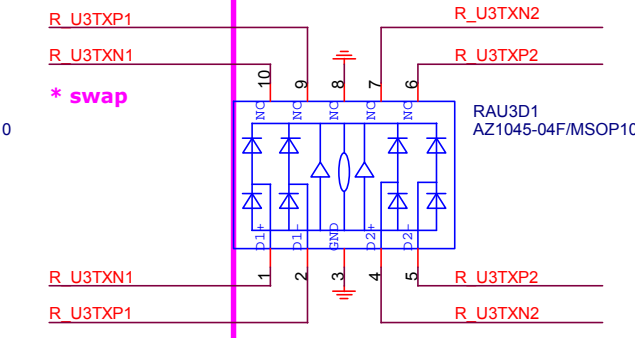


ESD

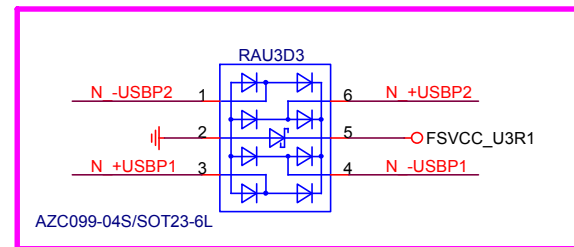
NET 可自行調整



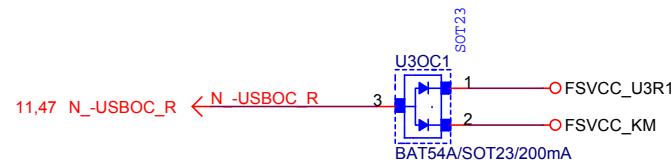
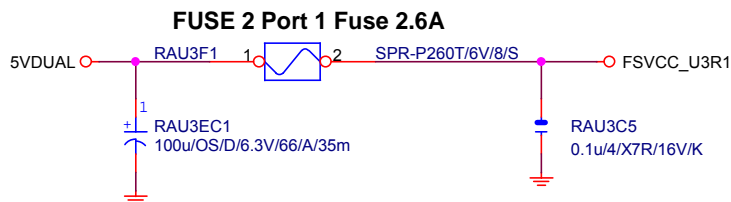
NET 可自行調整



NET 可自行調整

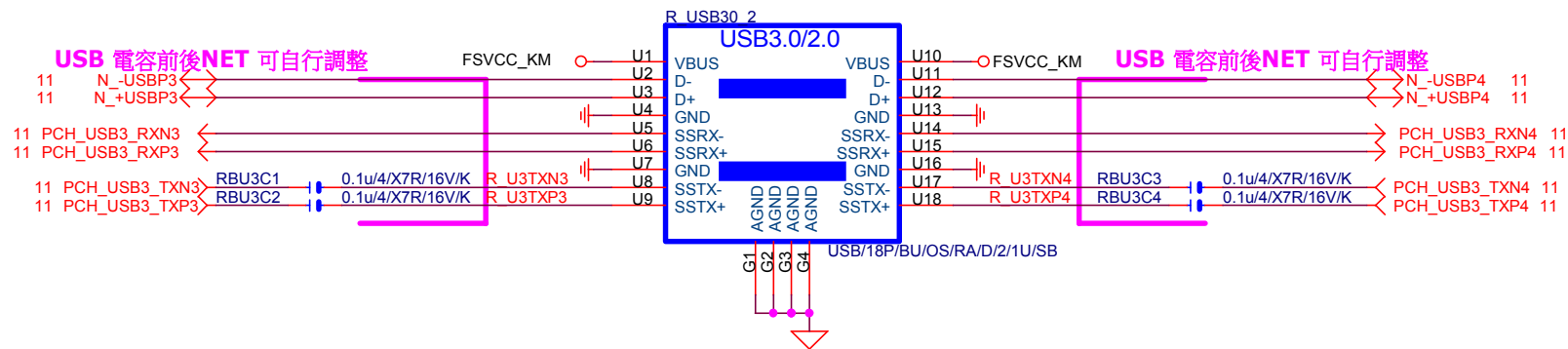


FUSE

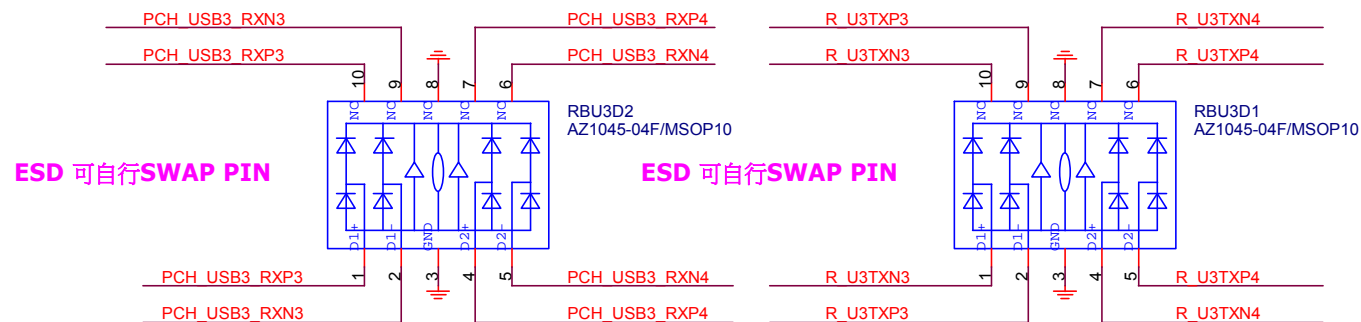


Gigabyte Technology

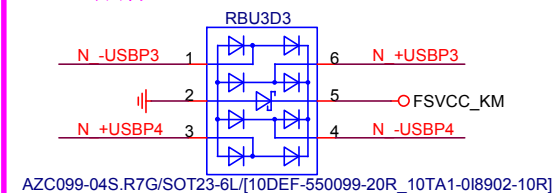
Title			
R_USB30,USB_OC			
Size	Document Number	GA-B250M-EVO	
Custom			Rev 1.0
Date:	Monday, November 14, 2016	Sheet	45 of 53



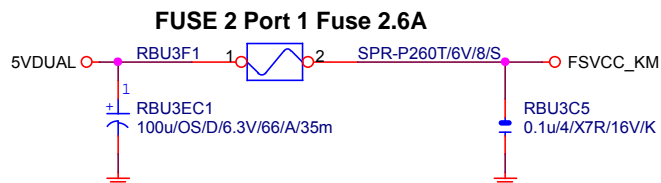
ESD



ESD 可自行SWAP PIN



FUSE



Gigabyte Technology

Title

R_USB30,USB_OC

Size
Custom

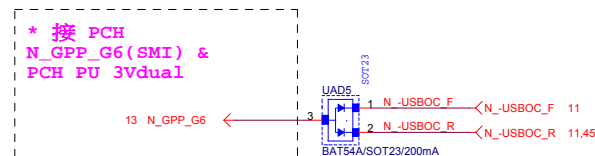
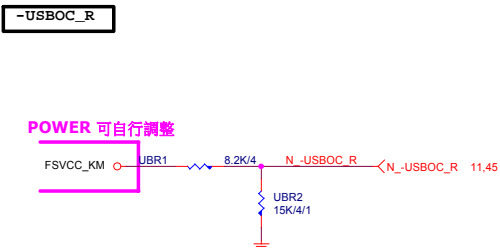
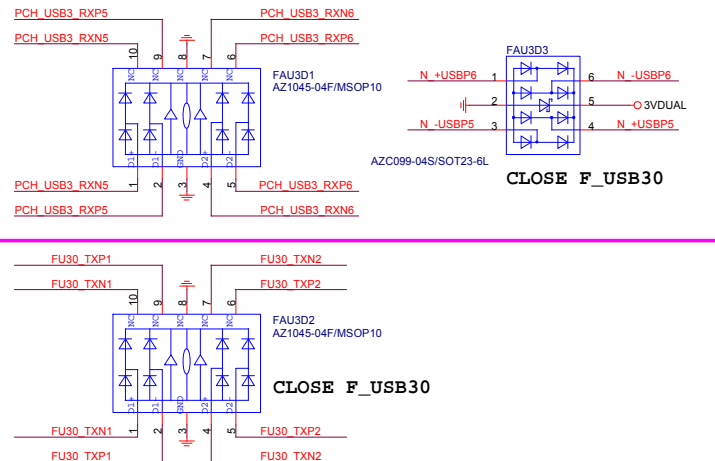
Document Number

GA-B250M-EVO

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1.0

Date: Monday, November 14, 2016

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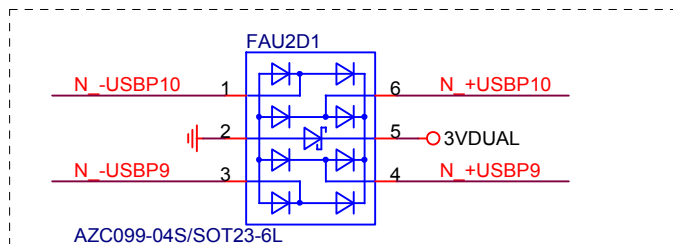
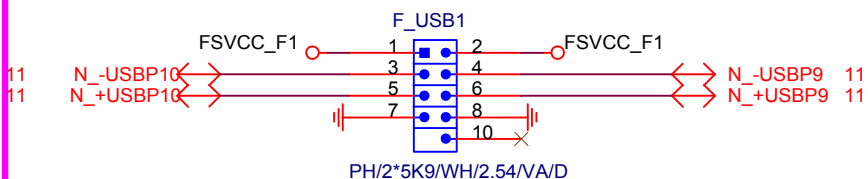


Rev: 0.7

FRONT USB1

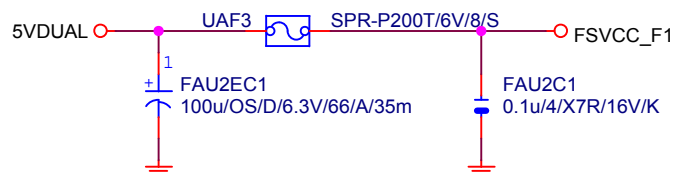
NET 可變

FUSB2X5-HS



Close to connector

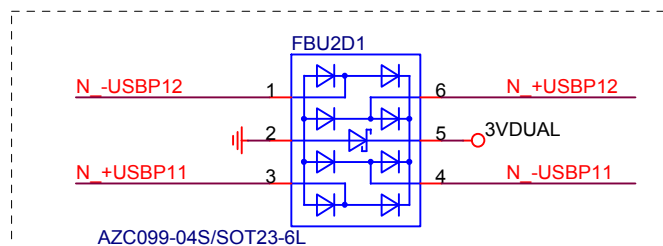
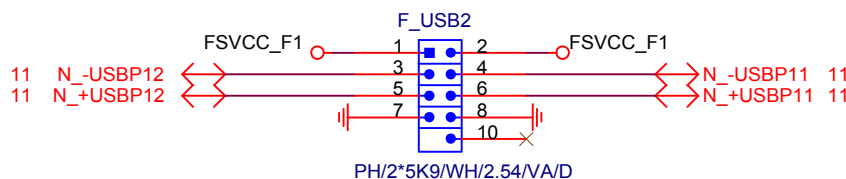
FUSE 2 Port 1 Fuse 2A



FRONT USB2

NET 可變

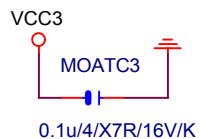
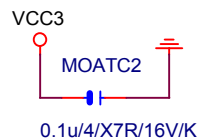
FUSB2X5-HS



Close to connector

FUSE 2 Port 1 Fuse 2A

F_USB 2.0 OC SIGNAL



Gigabyte Technology

Title

USB2.0

Size
A

Document Number

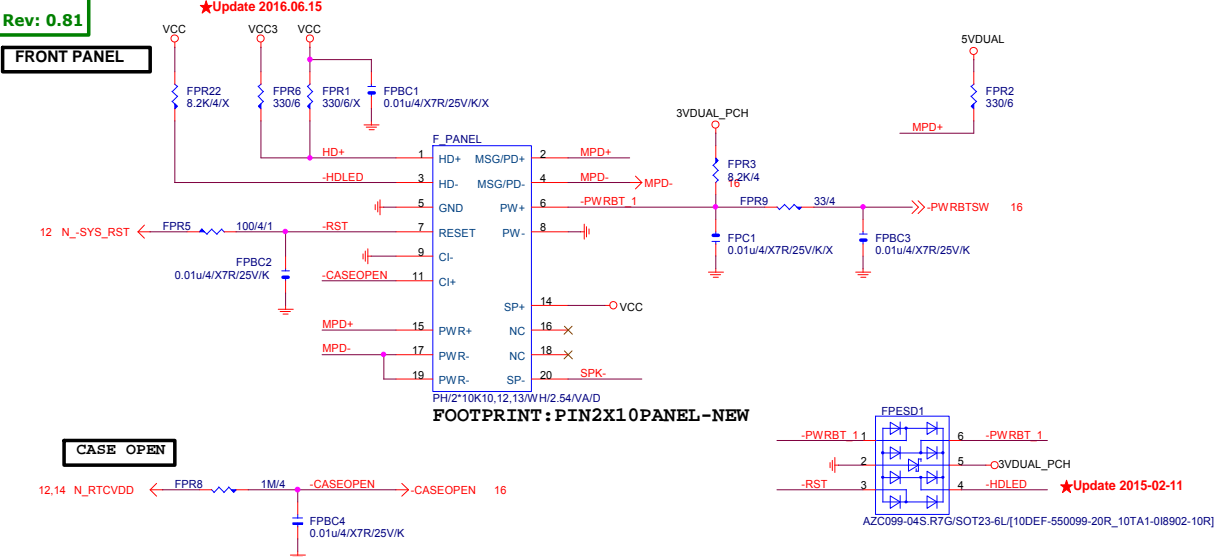
GA-B250M-EVO

Rev
1.0

Date: Monday, November 14, 2016

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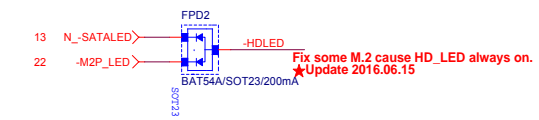
FRONT PANEL



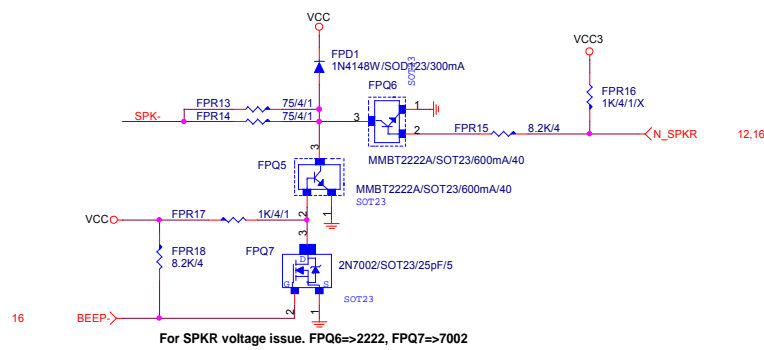
CASE OPEN

FRONT PANEL SHORT

SATA/M.2 LED

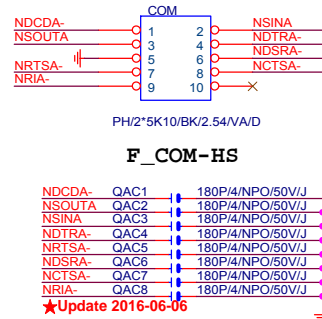
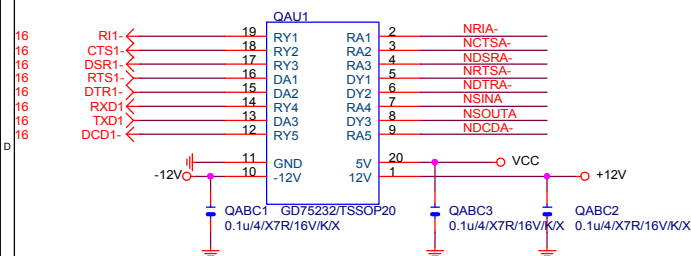


SPKR



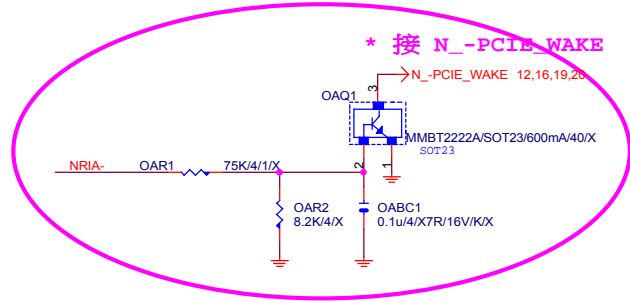
COM PORT

Rev: 0.7



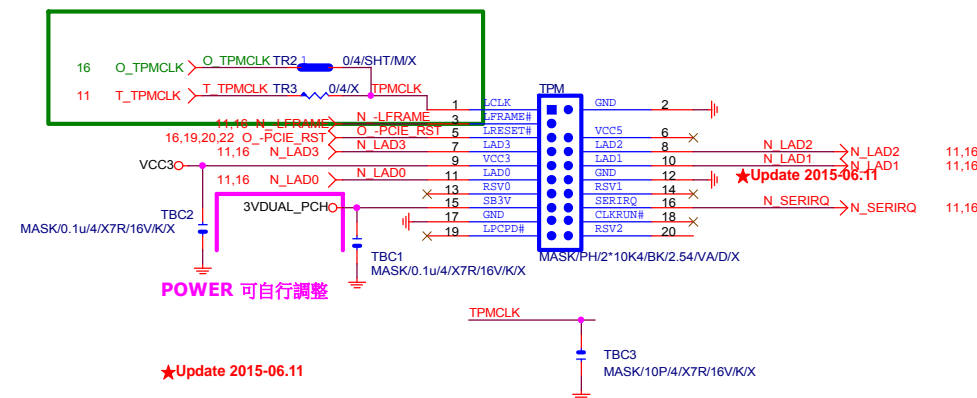
COM RI

N/A



LPT PORT

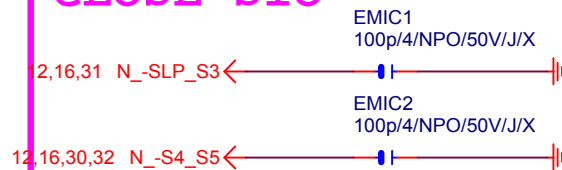
TPM CONNECT



Thunderbolt

★Update 2015-12-29

CLOSE SIO

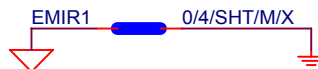


CLOSE PCH



close to PCH (NR17)

R_USB30_2 & DVI

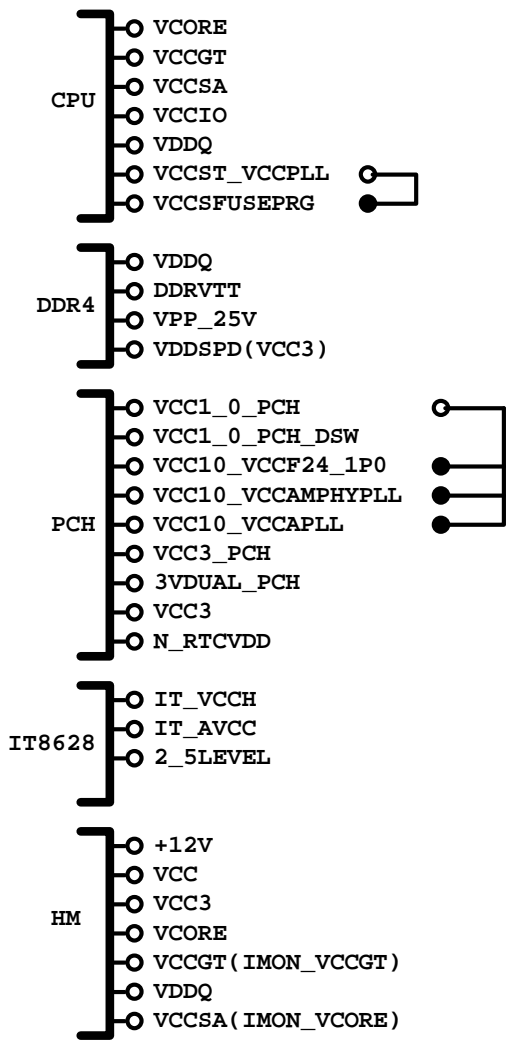


close to FRONT PANEL

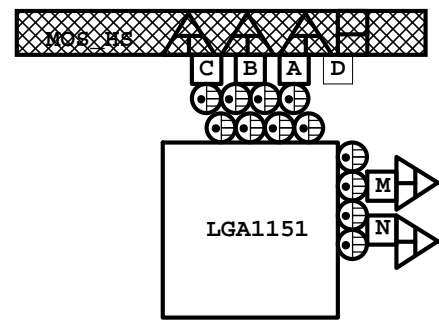
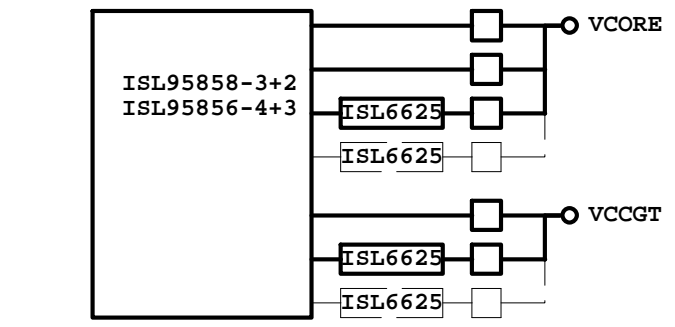
GIGABYTE™

Title		
EMI/ESD		
Size A	Document Number GA-B250M-EVO	Rev 1.0
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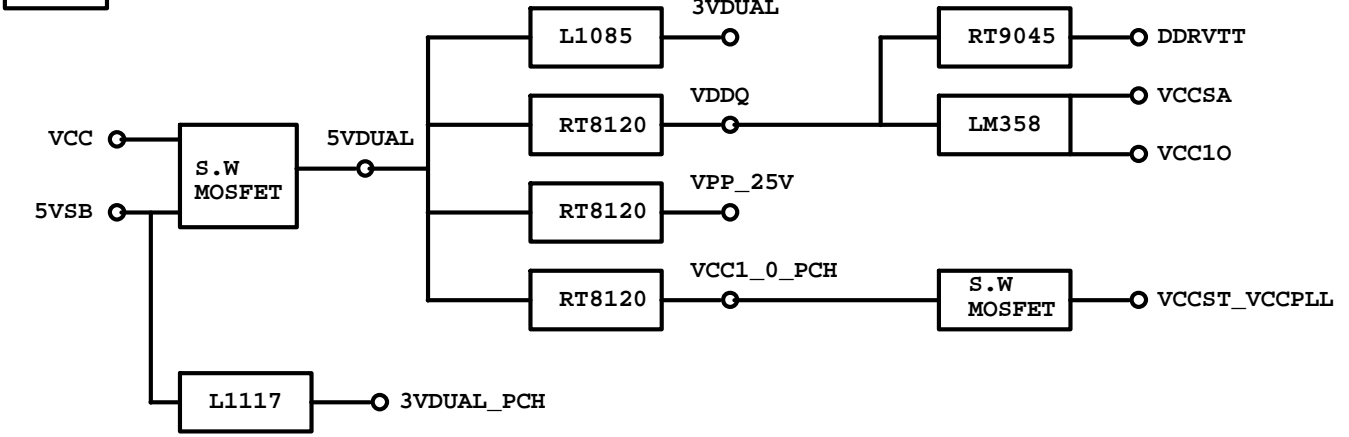
POWER BLOCK MAP



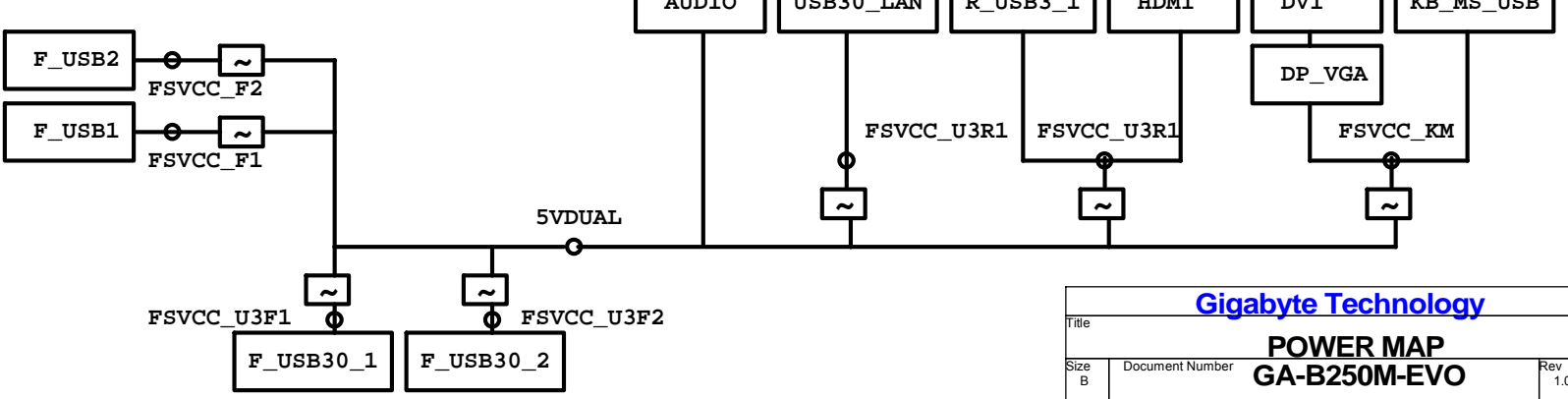
VCORE/VCCGT

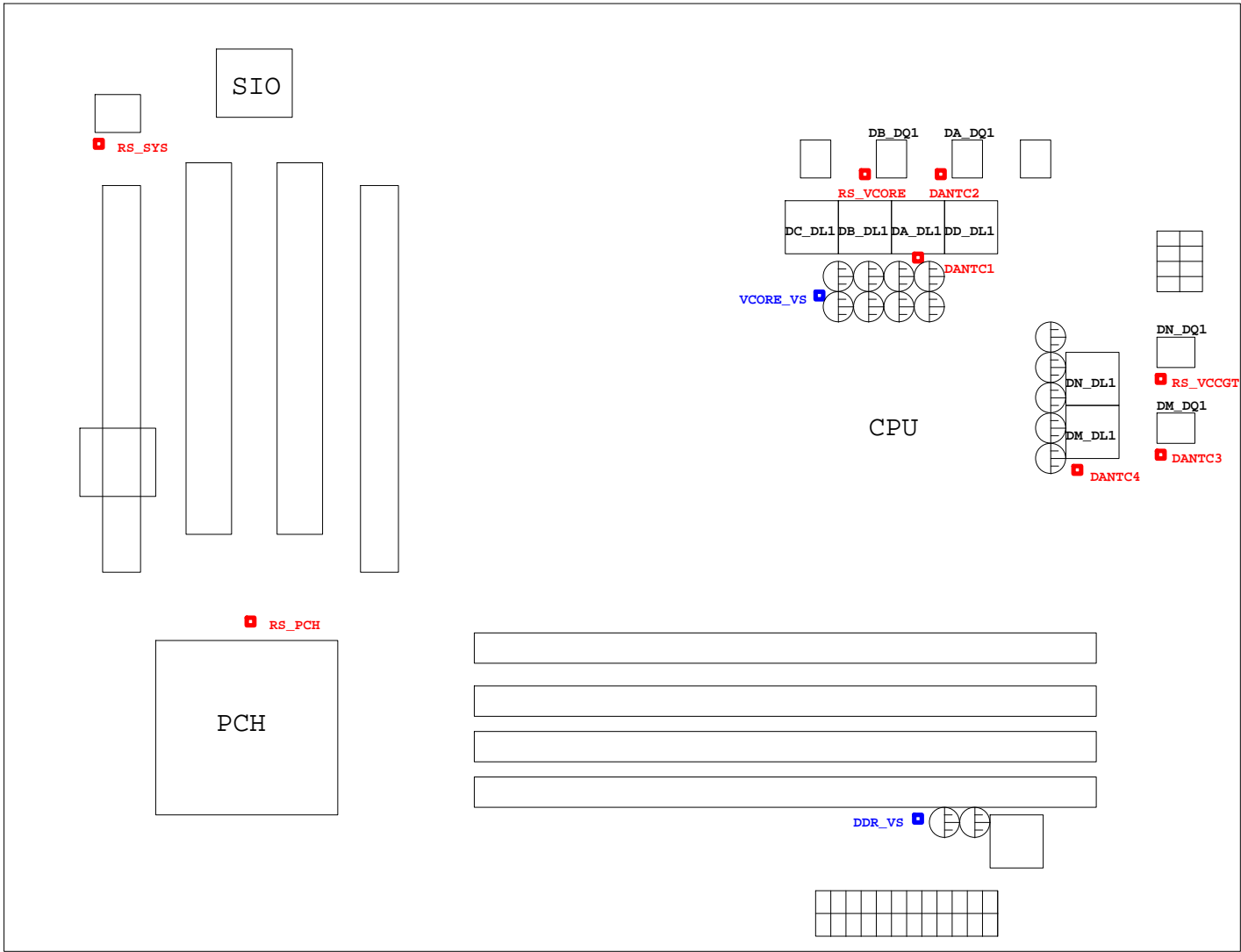


POWER



FUSE POWER F/R





熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	N/A
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	N/A
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ1	N/A
RS_VCCGT	DN_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A